



Multicore, WCET and IEC-61508 certification of fail-safe mixed- criticality systems

WCET Workshop
Lund (7th July)



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01

Context

02

Multicore is what you need / what you will have

03

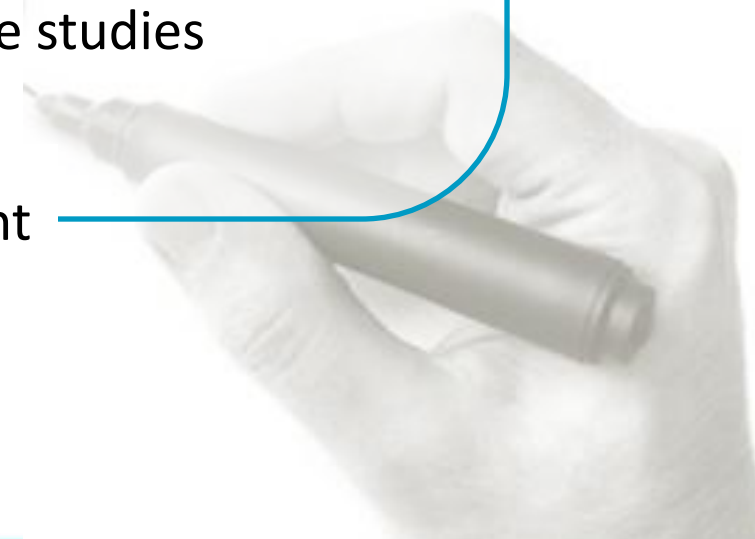
The need and opportunity

04

Wind turbine and railway case studies

05

Conclusions and lessons learnt



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01

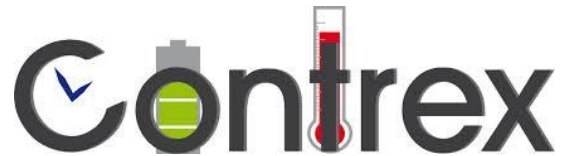
Context



DREAMS



MultiPARTES



Contrex



CERTAINTY



PROXIMA



TACLE
Timing Analysis on Code-Level



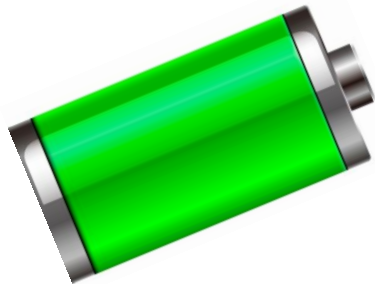
EMC²



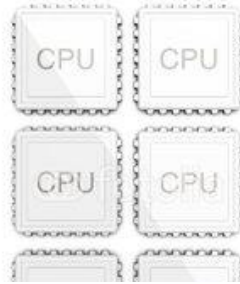
P-SOCRATES

Market Pull

Highly Reliable
Scalable
Available



Technology Push



WCET

INTERNATIONAL STANDARD IEC 61508-1

First edition 1999-12

BASIC SAFETY PUBLICATION

Functional safety of electrical/electronic/programmable electronic safety-related systems – Part 1: General requirements

This English-language version is derived from the original bilingual publication by leaving out all French-language pages. Missing page numbers correspond to the French-language pages.



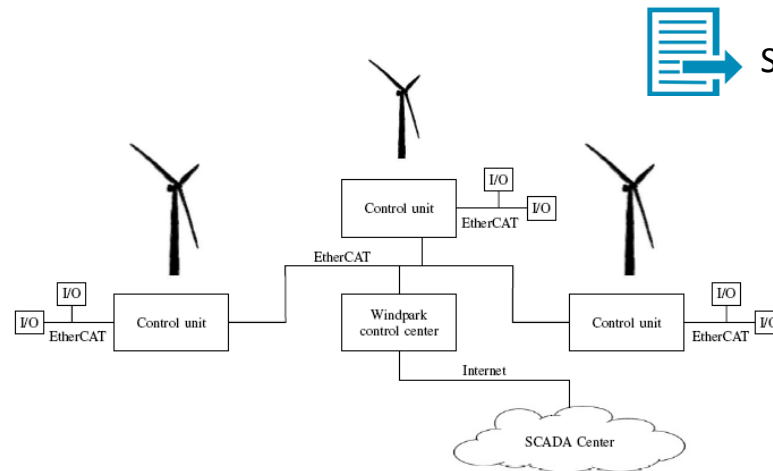
Reference number IEC 61508-1:1999(E)

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Product H2020



- ◇ A modern off-shore wind turbine dependable control system manages [1,2]:
 - **I/Os:** up to three thousand inputs / outputs.
 - **Function & Nodes:** several hundreds of functions distributed over several hundred of nodes.
 - **Distributed:** grouped into eight subsystems interconnected with a fieldbus.
 - **Software:** several hundred thousand lines of code.



Source: www.alstom.com

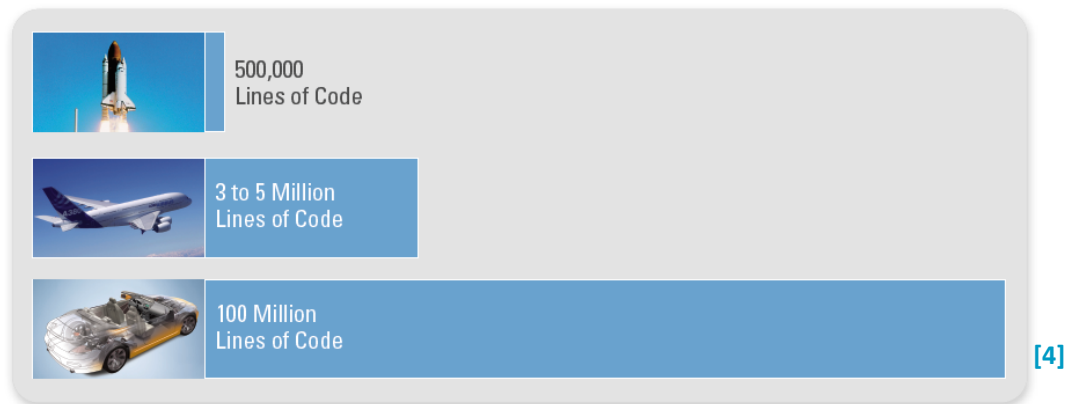


[1] Perez, J., et al. (2014). A safety concept for a wind power mixed-criticality embedded system based on multicore partitioning. Functional Safety in Industry Application, 11th International TÜV Rheinland Symposium, Cologne, Germany.

[2] Perez, J., et al. (2014). "A safety certification strategy for IEC-61508 compliant industrial mixed-criticality systems based on multicore partitioning." Euromicro DSD/SEAA Verona, Italy.

◇ Automotive domain:

- The software component in high-end cars currently totals around 20 million lines of code, deployed on as many as 70 ECUs [1].
- Automotive electronics accounts for some 30 % of overall production costs and is rising steadily [1].
- A premium car implements about 270 functions that a user interacts with, deployed over 67 independent embedded platforms, amounting to about 65 megabytes of binary code [2].



[1] Darren Buttle, ETAS GmbH, Germany, Real-Time in the Prime-Time, ECRTS (KEYNOTE TALK), 2012.

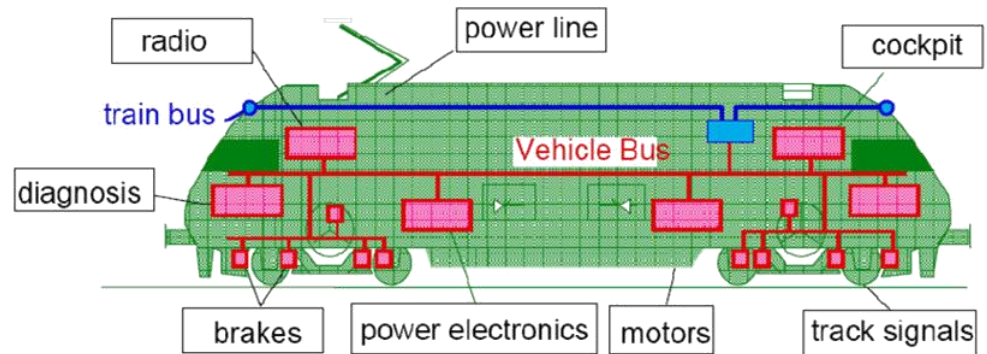
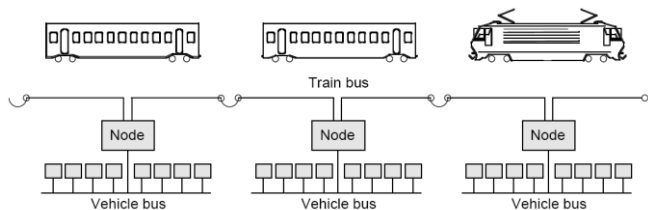
[2] Christian Salzmann and Thomas Stauner. Automotive software engineering. In Languages for System Specification, pages 333–347. Springer US, 2004.

[3] Lehold, J. Communication Requirements for Automotive Systems. 5th IEEE Workshop on Factory Communication Systems (WCFS). Wien, 2004.

[4] National Instruments, How engineers are reinventing the automobile,, <http://www.ni.com/newsletter/51684/en/> , 2013.

◇ (On-board) railway domain:

- The ever increasing request for safety, better performance, energy efficient, environmentally friendly and cost reduction in modern railway trains have forced the introduction of sophisticated dependable embedded systems [1].
- The number of ECUs (Electric Control Units) within a train system is of the order of a few hundred [2,3].
- Groups of distributed embedded systems:
 - Train Control Unit.
 - Railway Signalling (e.g. ETCS).
 - Traction Control.
 - Brake Control.
 - Etc.



[1] The European Rail Research Advisory Council (ERRAC), Joint Strategy for European Rail Research 2020.

[2] Kirrmann, H. and P. A. Zuber (2001). "The IEC/IEEE Train Communication Network." IEEE Micro vol. 21, no. 2: 81-92.

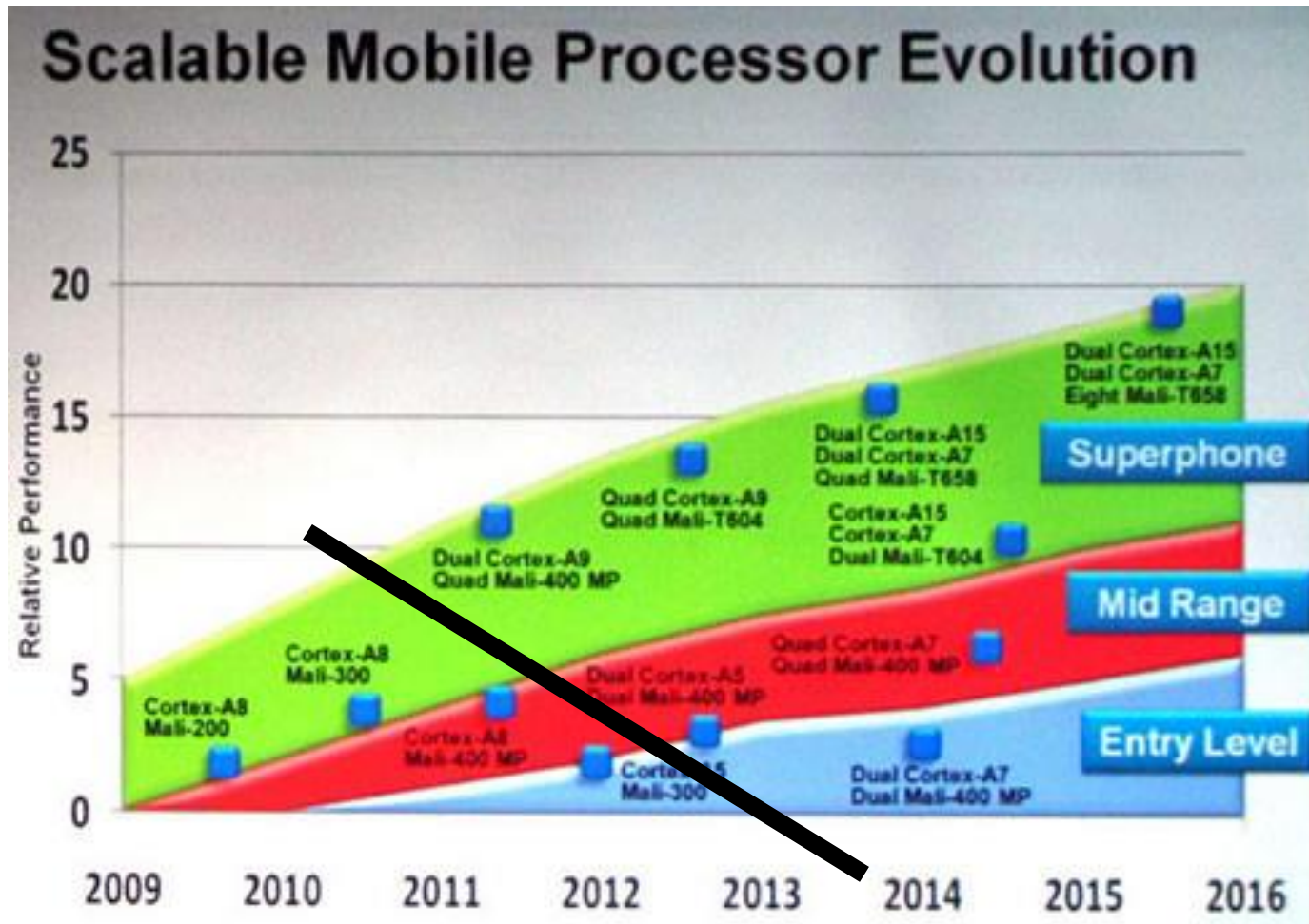
[3] F. Corbier, et al, *How Train Transportation Design Challenges can be addressed with Simulation-based Virtual Prototyping for Distributed Systems*, 3rd European congress Embedded Real Time Software (ERTS), France, 2006.

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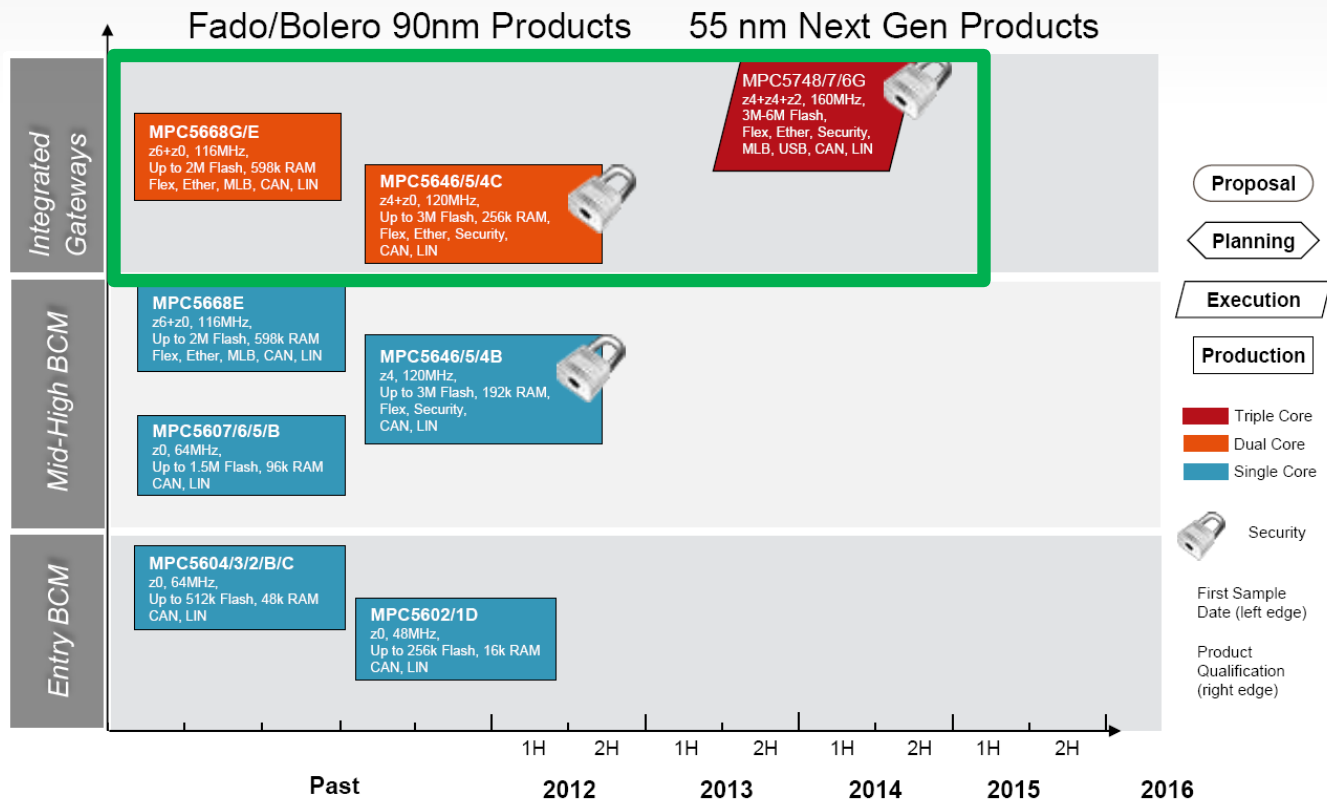
02

Multicore is what you need...
Multicore is what you will have...

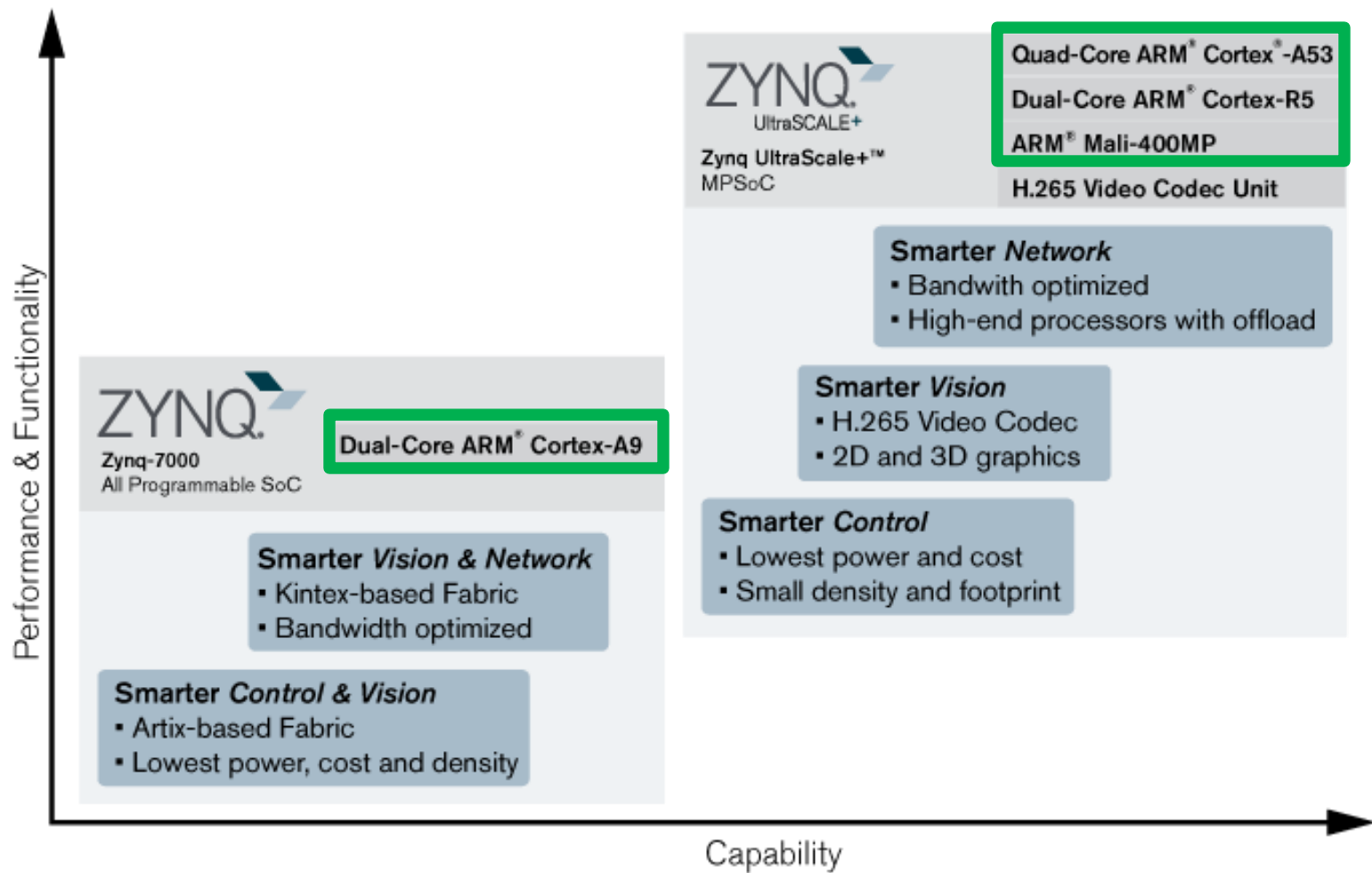


Source: www.arm.com

32-bit Body Electronics MCU Roadmap

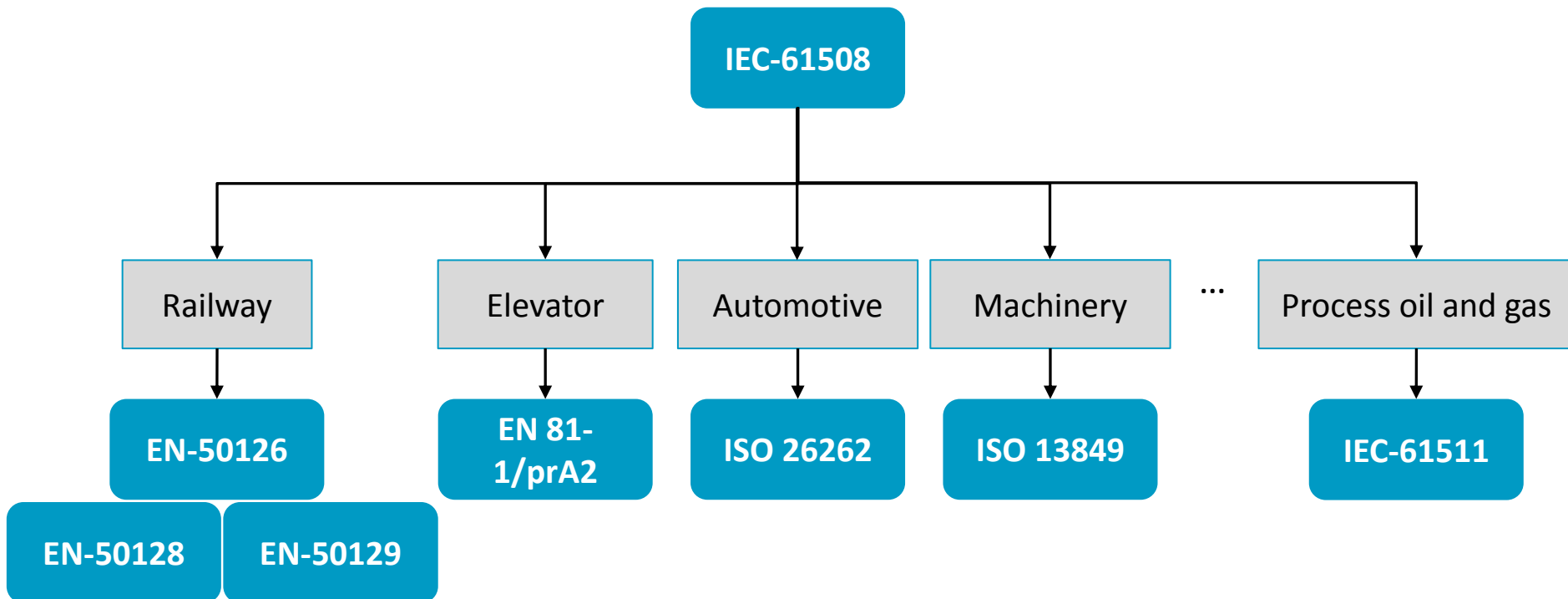


Source: www.freescale.com



Source: www.xilinx.com

- ◆ IEC-61508: Functional safety of electrical / electronic / programmable electronic safety-related systems.



IEC-61508	ISO-26262
<p>(7.4.2.2) The design method chosen shall possess features that facilitate the expression of: [...] (4) <u>timing constraints</u></p>	<p>(7.4.5) The software architectural design shall describe dynamic design aspects of the software components, including: [...] <u>the temporal constraints</u></p>
<p>(IEC-61508-3 Annex F) Non-interference between software elements on a single computer: F.5: <u>cyclic scheduling</u> algorithm which gives each element a defined time slice supported by worst case execution time analysis of each element to demonstrate statically that the timing requirements for each element are met</p>	<p>(7.4.17) An <u>upper estimation</u> of required resources for the embedded software shall be made, including: (a) <u>the execution time</u>;</p> <p>(Annex D) Freedom from interference between software elements D2.2: With respect to timing constraints the effects of faults such as [...] <u>incorrect allocation of execution time</u> shall be considered and mechanisms such as <u>cyclic execution scheduling</u> can be considered.</p>

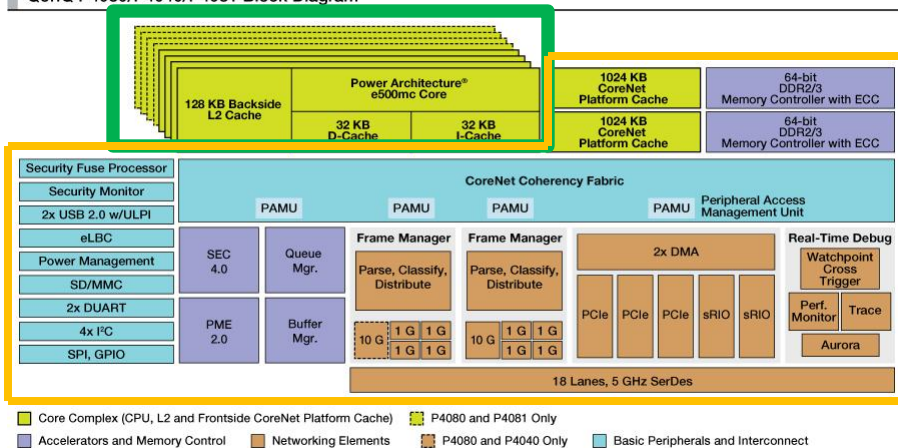




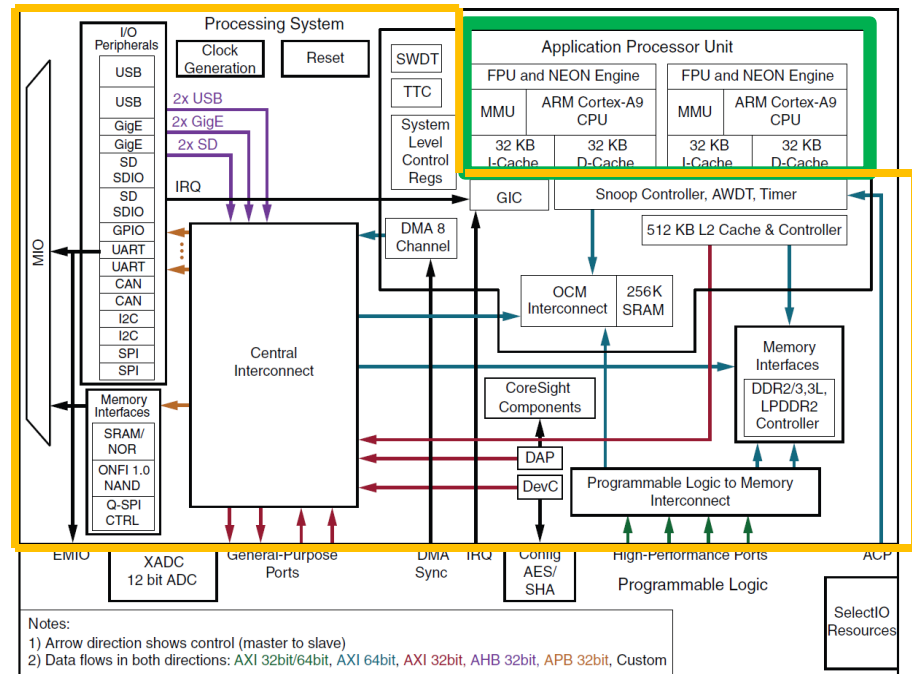
Worst Case Execution Time (WCET)



QorIQ P4080/P4040/P4081 Block Diagram



■ Core Complex (CPU, L2 and Frontside CoreNet Platform Cache)
 ■ P4080 and P4081 Only
 ■ Accelerators and Memory Control
 ■ Networking Elements
 ■ P4080 and P4040 Only
 ■ Basic Peripherals and Interconnect



Notes:
 1) Arrow direction shows control (master to slave)
 2) Data flows in both directions: AXI 32bit/64bit, AXI 64bit, AXI 32bit, AHB 32bit, APB 32bit, Custom



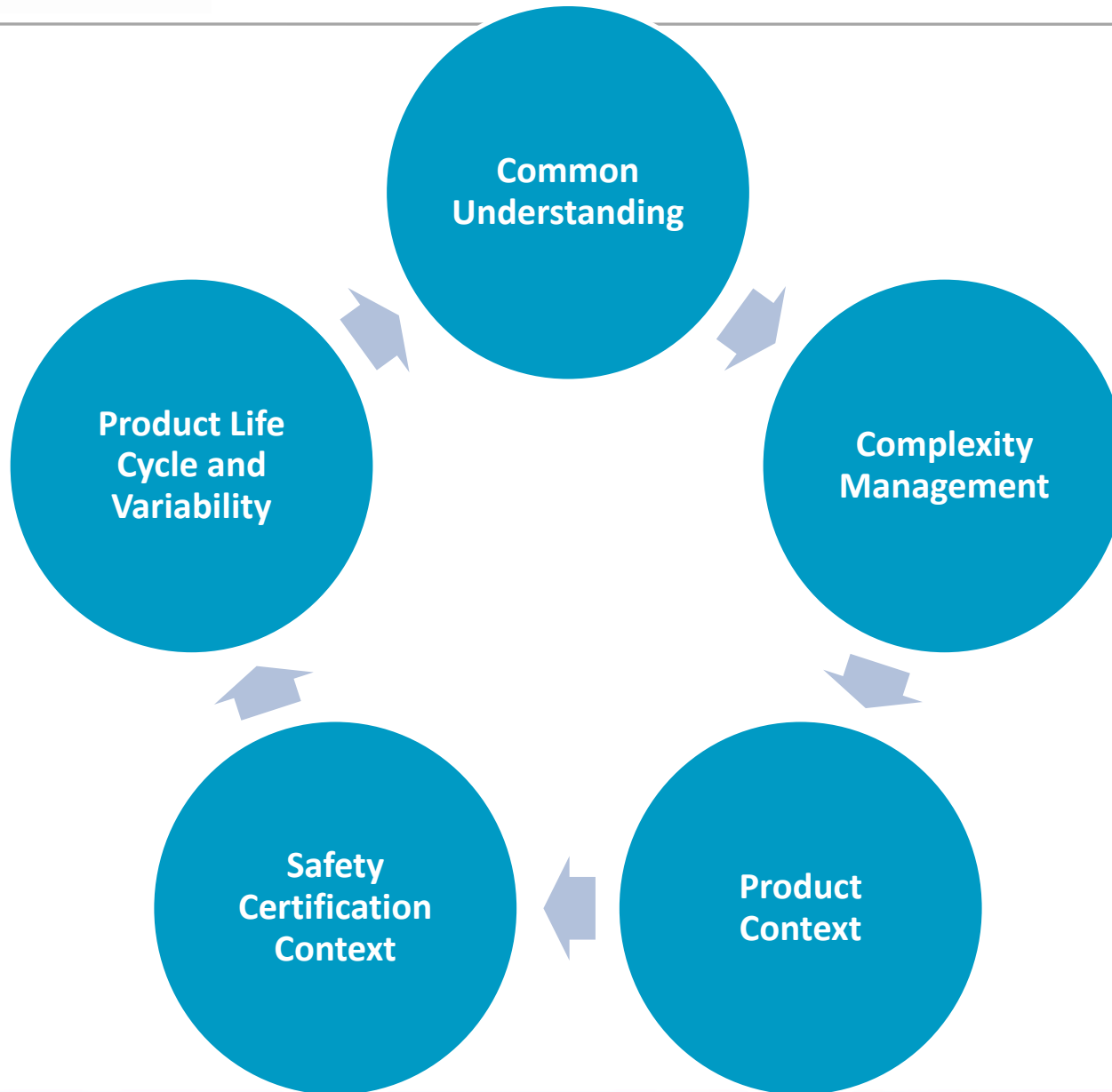
Source: www.freescale.com, www.xilinx.com

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03

The need and opportunity



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03-A

Common understanding

“

What then is time? If nobody asks me, I know what time is, but if I am asked then I am at a loss

”

what to say (St. Augustine)

- Safety

- Safety Critical
- Mission Critical

- Temporal isolation

- Modular certification (?)

- Scheduling:

- Vestas model



- Etc.

- Functional Safety

- Fail safe / Fail operational
- High demand / Low demand

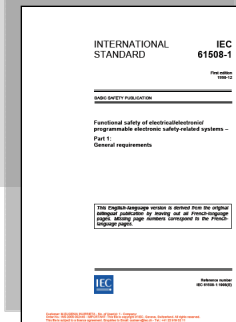
- Temporal independence

- Compliant Item

- Scheduling (IEC-61508-3):

- Deterministic scheduling methods
 - Time Triggered Architecture
 - Cyclic scheduling

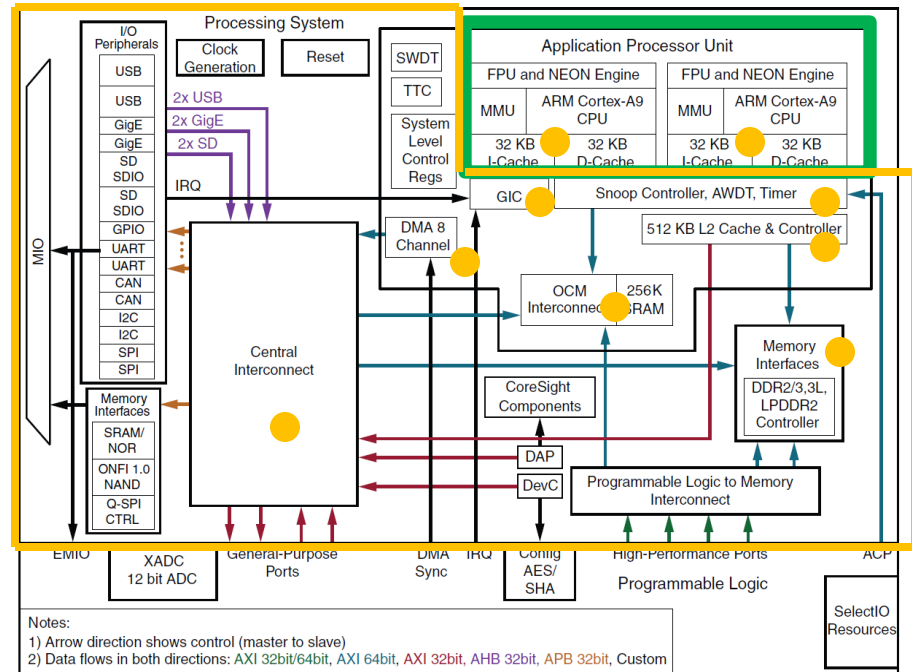
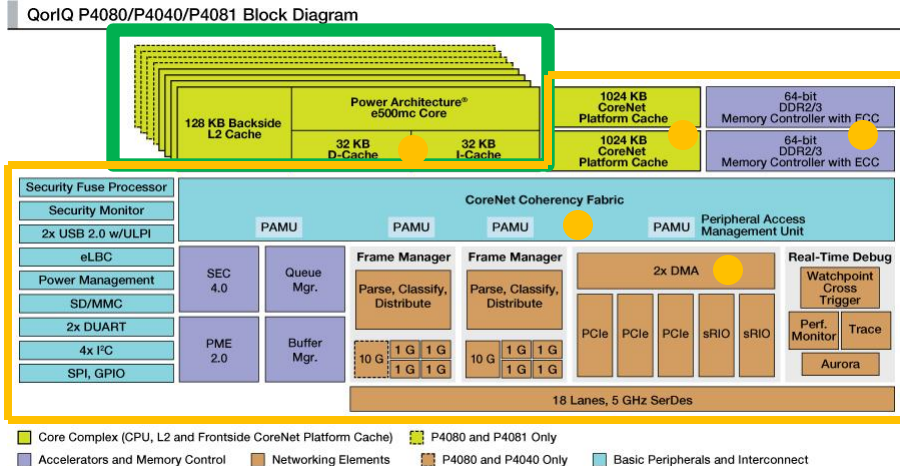
- Etc.





- ◆ Temporal & Spatial independence, e.g., Shared resources (e.g., memory, cache, bus, interrupts) [1]

Which is the time-scale of the temporal interference?



[1] Kotaba, O., et al. (2013). Multicore In Real-Time Systems – Temporal Isolation Challenges Due To Shared Resources. Workshop on Industry-Driven Approaches for Cost-effective Certification of Safety-Critical, Mixed-Criticality Systems (WICERT). Dresden (Germany).



Source: www.freescale.com, www.xilinx.com

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03-B

Complexity Management

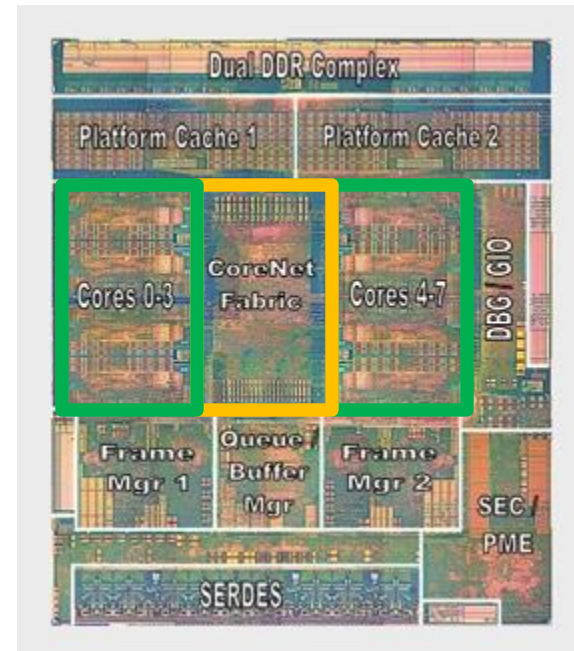
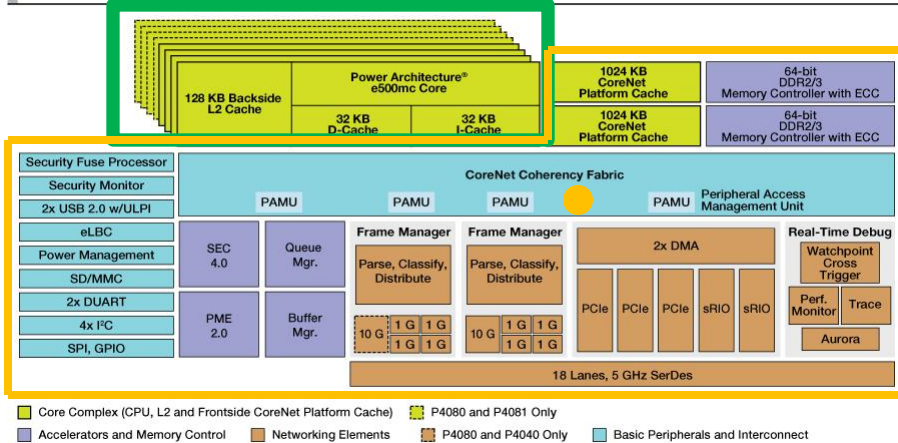
“ Simplicity does not precede complexity, but follows it. ” (Alan Perlis)

“ Fools ignore complexity. Pragmatists suffer it. Some can avoid it. Geniuses remove it. ” (Alan Perlis)



- ◆ Complex (new) hardware components, e.g., Core interconnect fabric
- ◆ Lack of detailed documentation

QorIQ P4080/P4040/P4081 Block Diagram



[1] <http://www.advancedsubstratenews.com/2009/12/multicores-perfect-balance/>



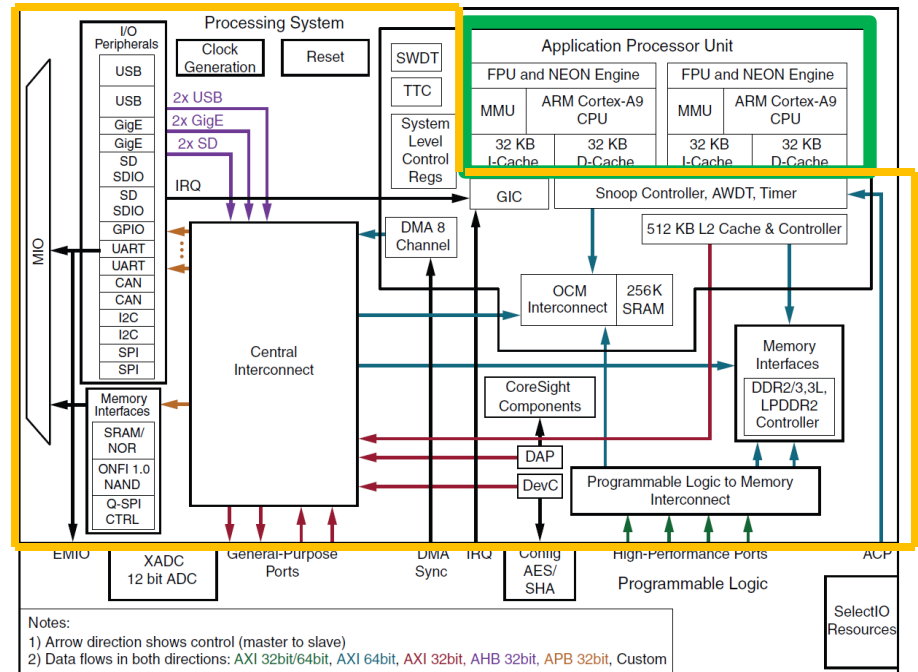
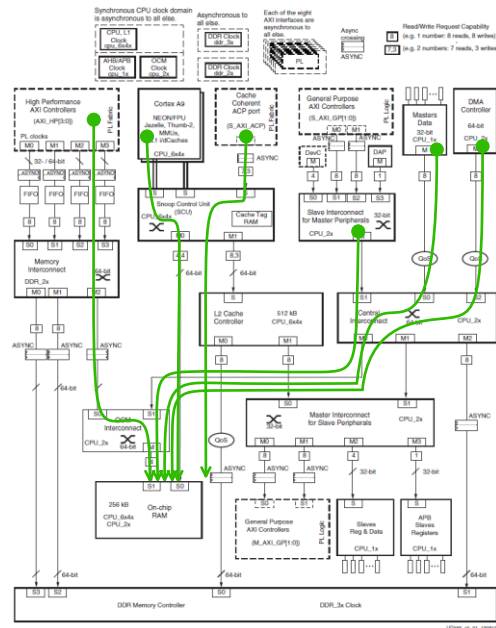
Source: www.freescale.com, www.xilinx.com



◆ Interference among safety related and non safety related functions, e.g.

- Safe configuration.
- Safe startup and boot.
- Safe shutdown.
- Exclusive access to peripherals.
- Resource virtualization.

◆ Diagnosis



Source: www.freescale.com, www.xilinx.com



- ◇ Complexity: “the degree to which a system or component has a design or implementation that is difficult to understand and verify”
- ◇ Cognitive complexity and number four:
 - Human cognitive capabilities and four simultaneous relationships [1, 2]
 - Working memory capacity for up to four simultaneous chunks of information [3]
 - Quaternary relations are the most complex we can handle [1, 2]
 - Human working memory capacity limited also to about four chunks of information [3]
- ◇ Complexity Management [4]: abstraction, partition and segmentation

Simplicity does not precede complexity, but follows it.
(Alan Perlis)

Fools ignore complexity. Pragmatists suffer it. Some can avoid it. Geniuses remove it.
(Alan Perlis)

[1] Bernhard Rumpler et al. Considerations on the complexity of embedded real-time system design tasks. In IEEE International Conference on Computational Cybernetics (ICCC), Tallinn, Estonia, 2006..

[2] Graeme S. Halford et al. How many variables can humans process? Psychological Science, 16(1):70–76, 2005.

[3] Nelson Cowan. The magical number 4 in short-term memory: a reconsideration of mental storage capacity. Behavioral and Brain Sciences, 24(1):87–114, 2001.

[4] H. Kopetz. The complexity challenge in embedded system design. In 11th IEEE International Symposium on Object Oriented Real-Time Distributed Computing (ISORC), pages 3–12, 2008.

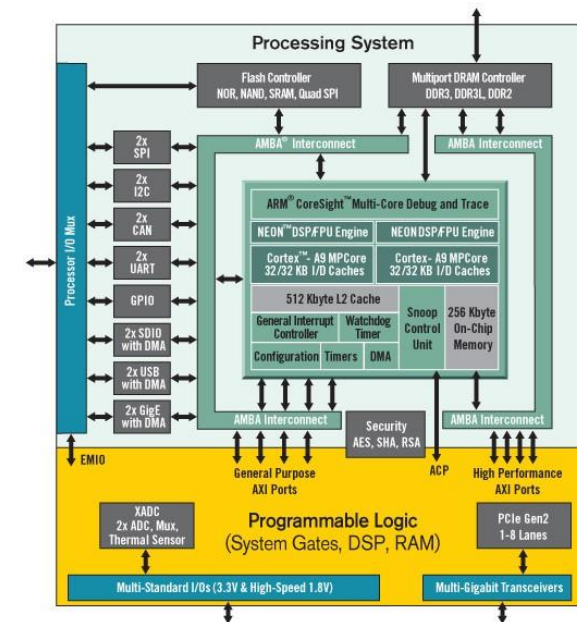
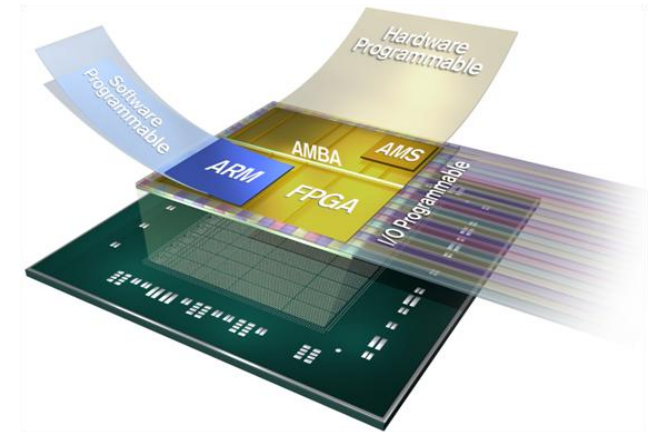
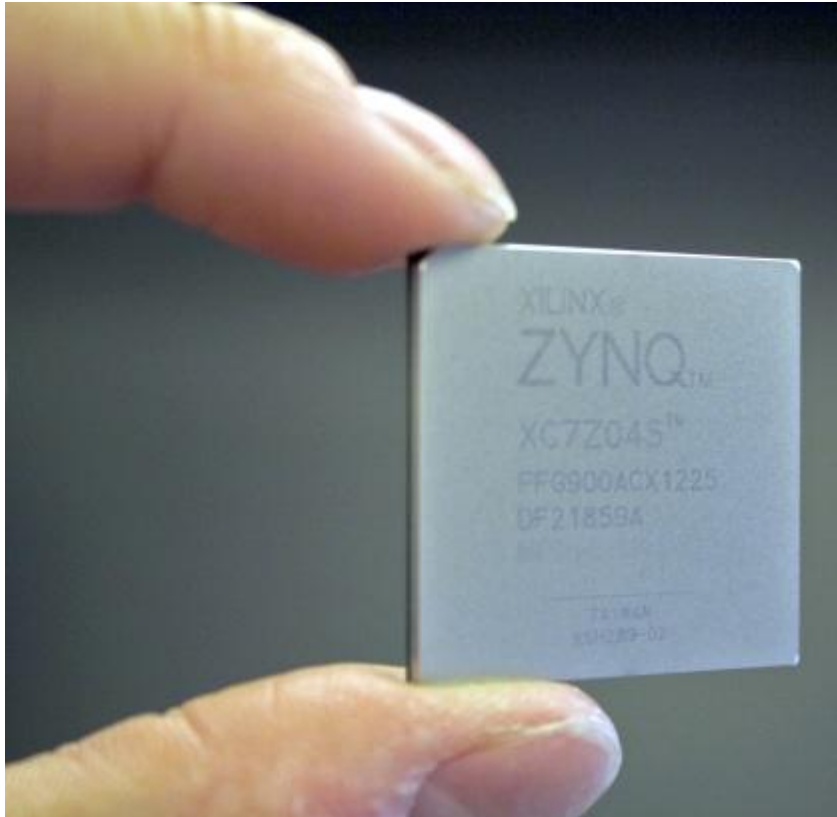


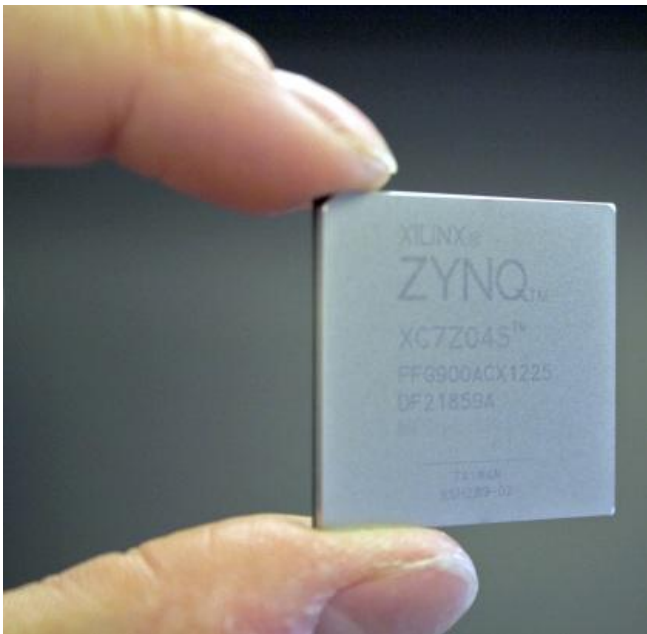
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03-C

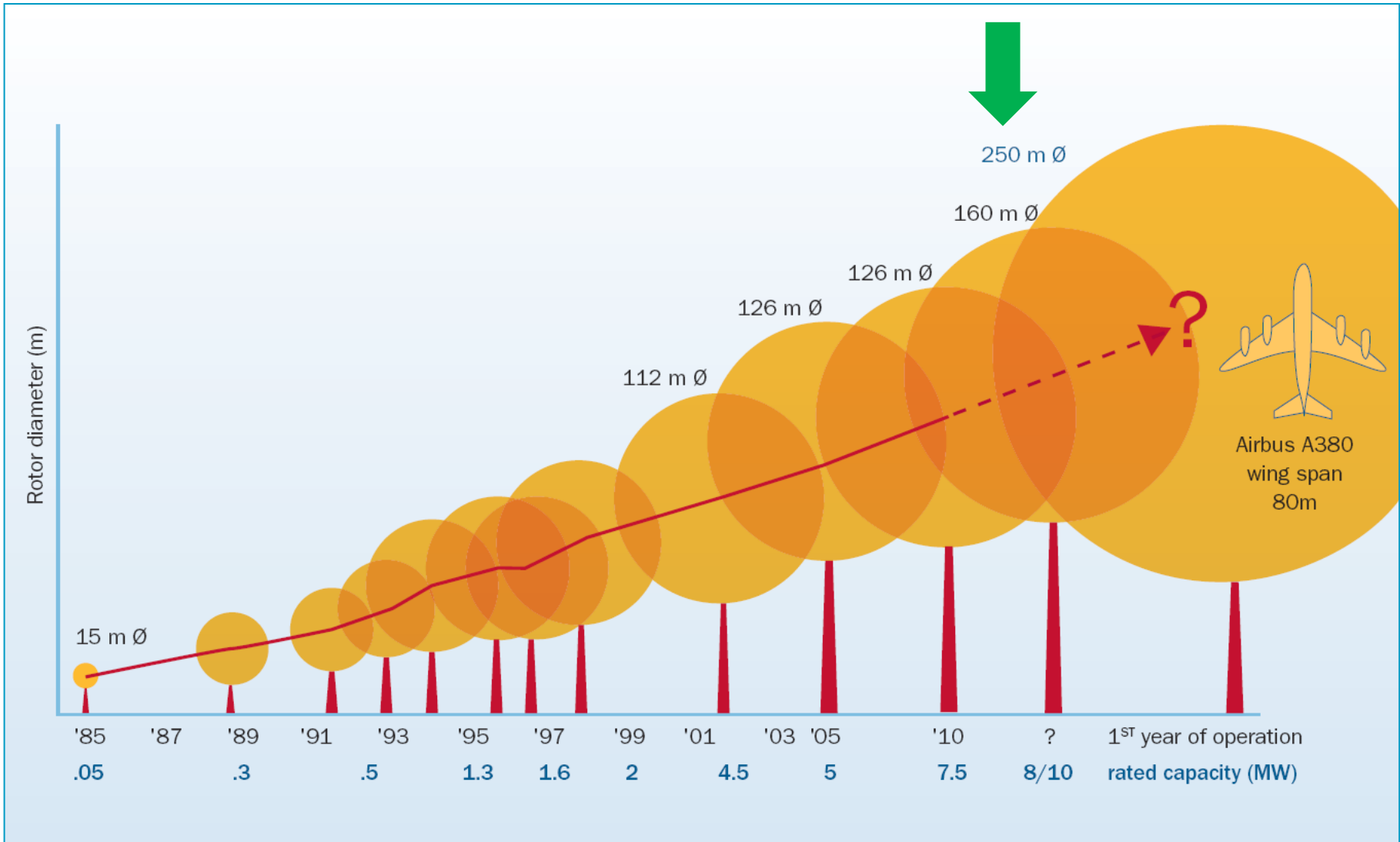
Product Context

“ However beautiful the strategy, you should occasionally look at the results. ” (Sir Winston Churchill)

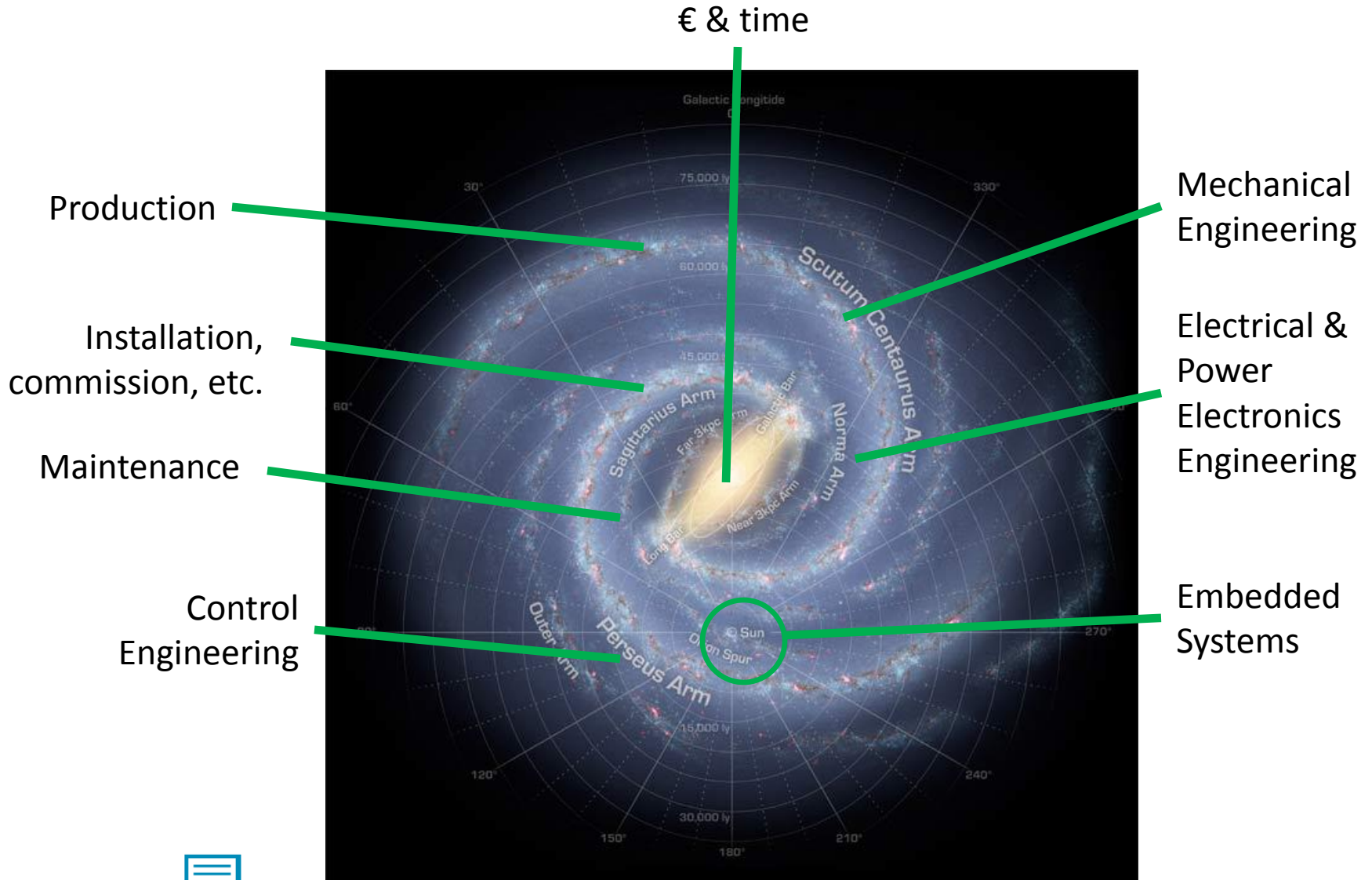




Source: www.xilinx.com, www.alstom.com



[1] Upwind – Design limits and solutions for very large wind turbines, March 2011



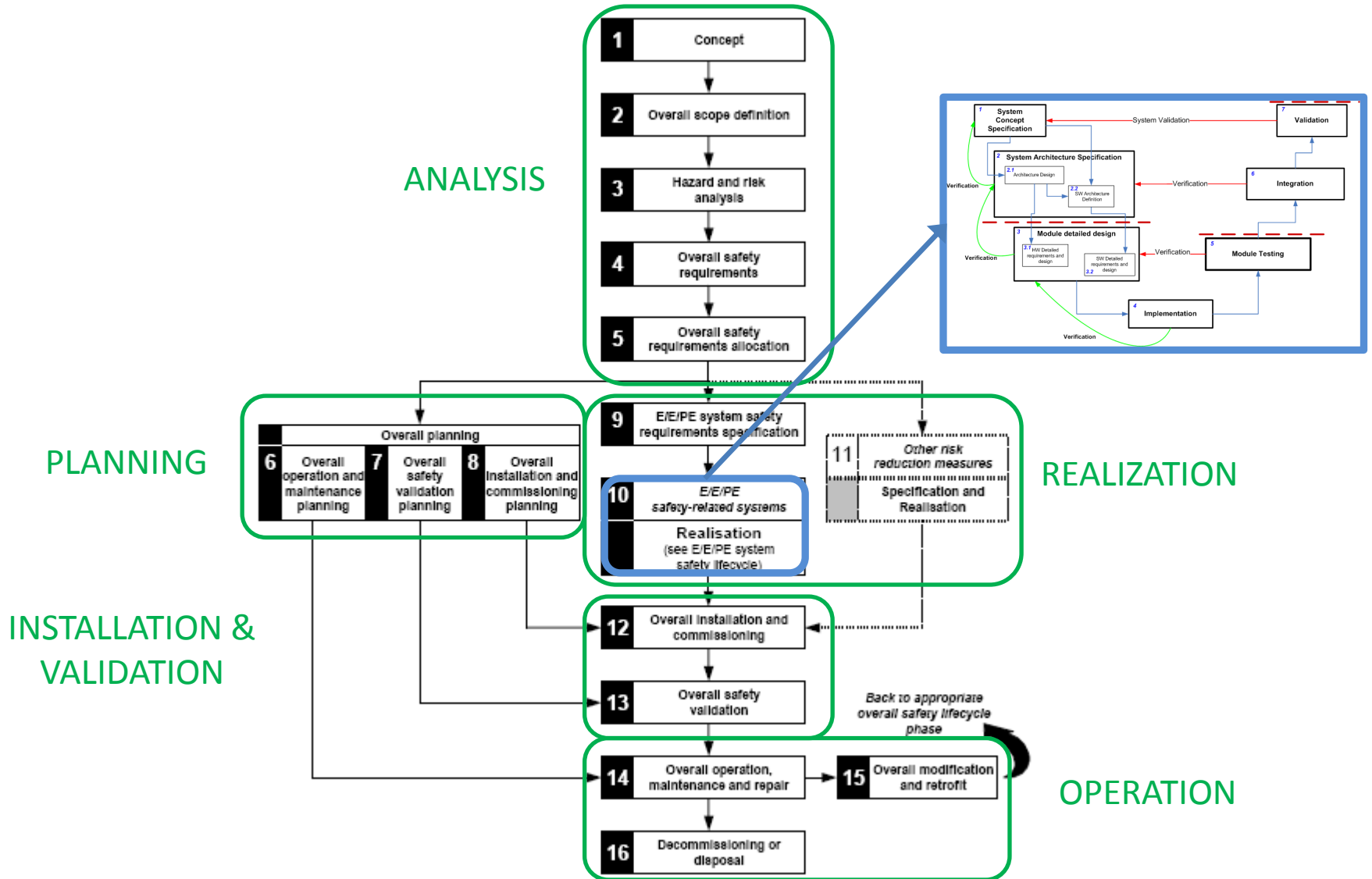
Source: <http://www.space.com/5448-images-milky-loses-arms.html>

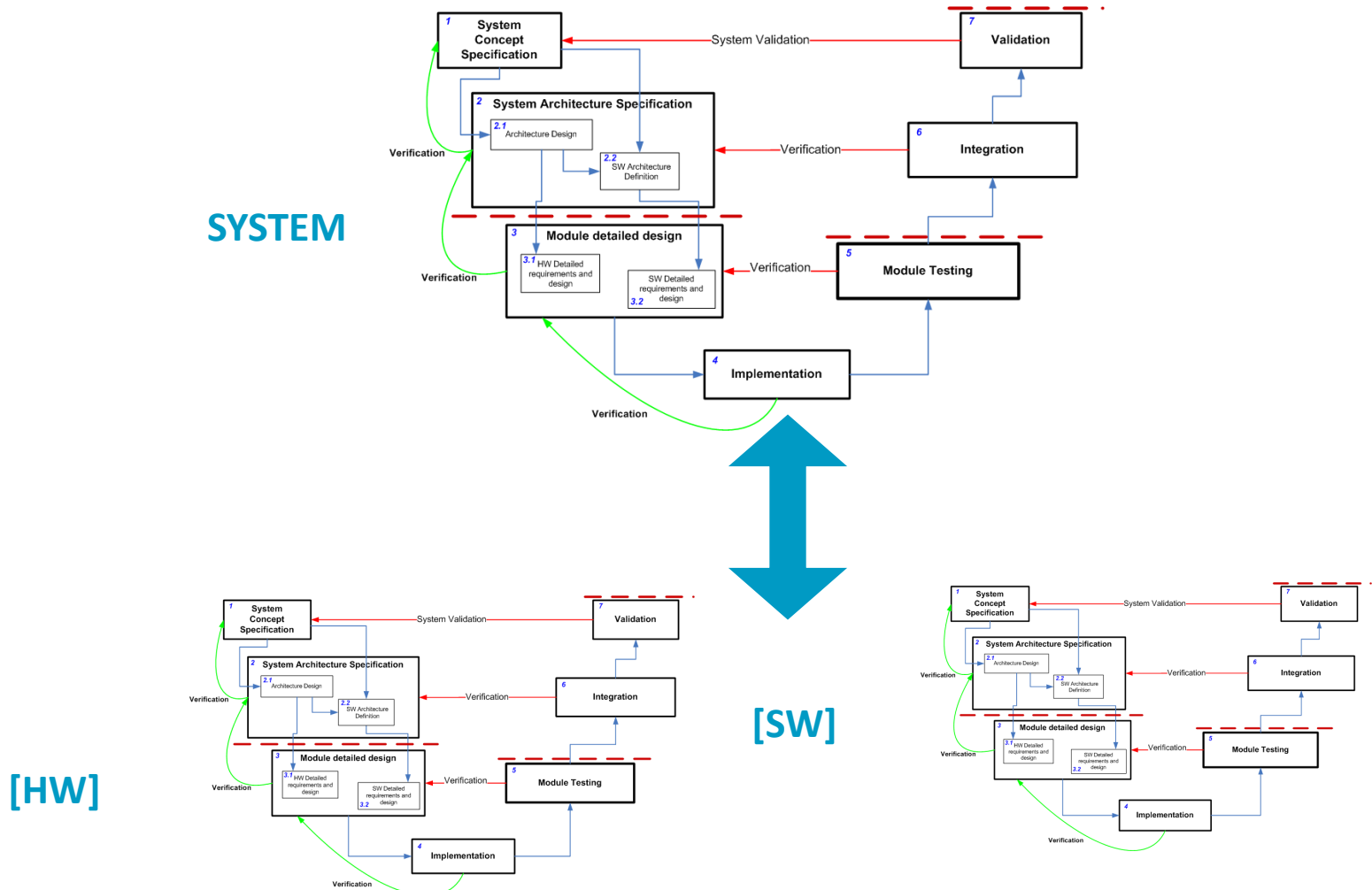
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03-D

Safety certification context

“ For me context is the key - from that comes the understanding of everything. ” (Kenneth Noland)



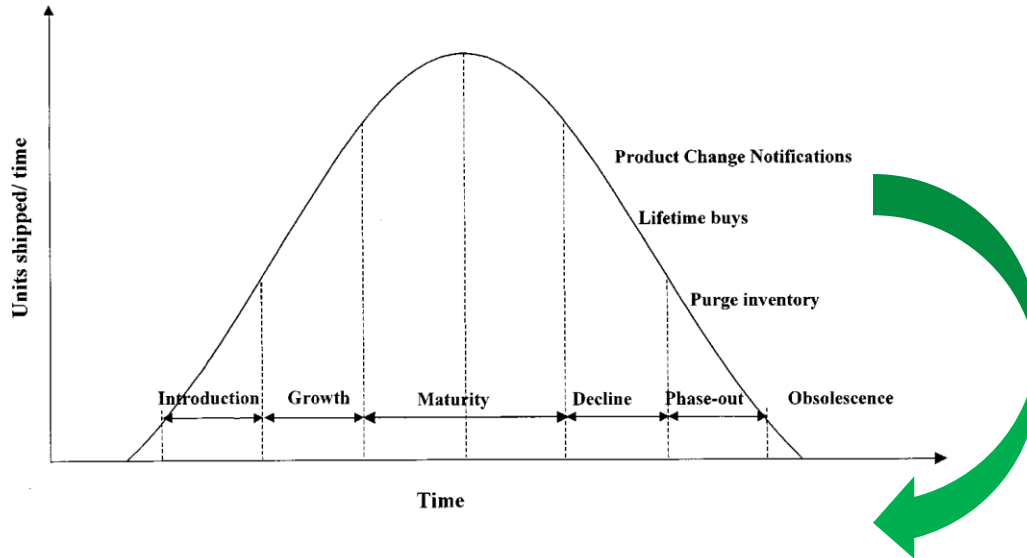


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03-E

Product Life Cycle & Variability

“ The ability to ask the right question is more than half the battle of finding the answer. ” (Thomas J. Watson)



Part life cycle curves [1]

5 years



40 years

[1] Pecht, M. G. and D. Das (2000). "Electronic part life cycle." *Components and Packaging Technologies*, IEEE Transactions on 23(1): 190-192.

◇ **Manufacturer**

◇ **Core Family**

- ARM
- X86
- PowerPC

◇ **Number of cores**

◇ **Timers**

◇ **Memory, e.g.,**

- Cache (hierarchy, size, type)
- RAM Memory (hierarchy, size, type)
- ROM Memory (e.g., Flash)
- External memory support (e.g. DDR3)

◇ **Buses, e.g.,**

- Internal bus (hierarchy, type, width)
- External bus interfaces (e.g. PCIe)
- Communication buses (e.g. CAN, Ethernet, I2C, I2S, SPI, USB)
- DMA

◇ **I/Os, e.g.,**

- GPIOs
- ADCs (type, resolution, number of channels)
- PWM

◇ **Safety compliance**

◇ **Target Application**

◇ **Temperature Range (e.g. Industrial)**

◇ **Clock Frequency**

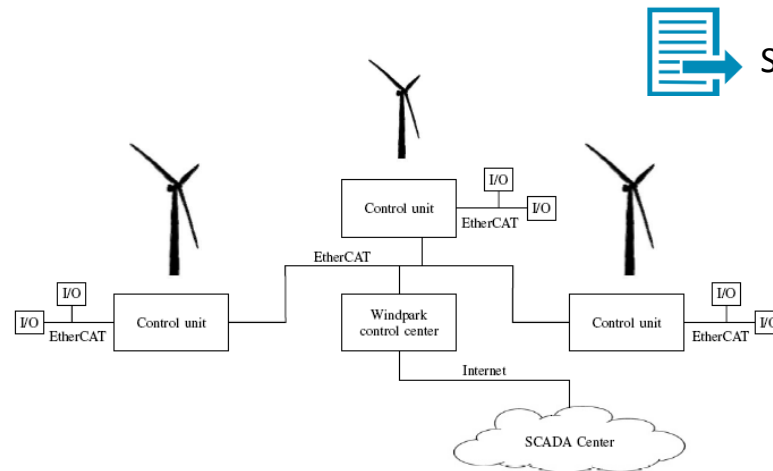
◇ **Supply voltage**

◇ **Package Type**

Wind turbine and railway case studies

“ Nihil est enim simul et inventum et perfectum. ” (Nothing is ever invented and perfected at the same time)
(Cicero, Brutus 71)

- ◇ A modern off-shore wind turbine dependable control system manages [1,2]:
 - **I/Os:** up to three thousand inputs / outputs.
 - **Function & Nodes:** several hundreds of functions distributed over several hundred of nodes.
 - **Distributed:** grouped into eight subsystems interconnected with a fieldbus.
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Source: www.alstom.com

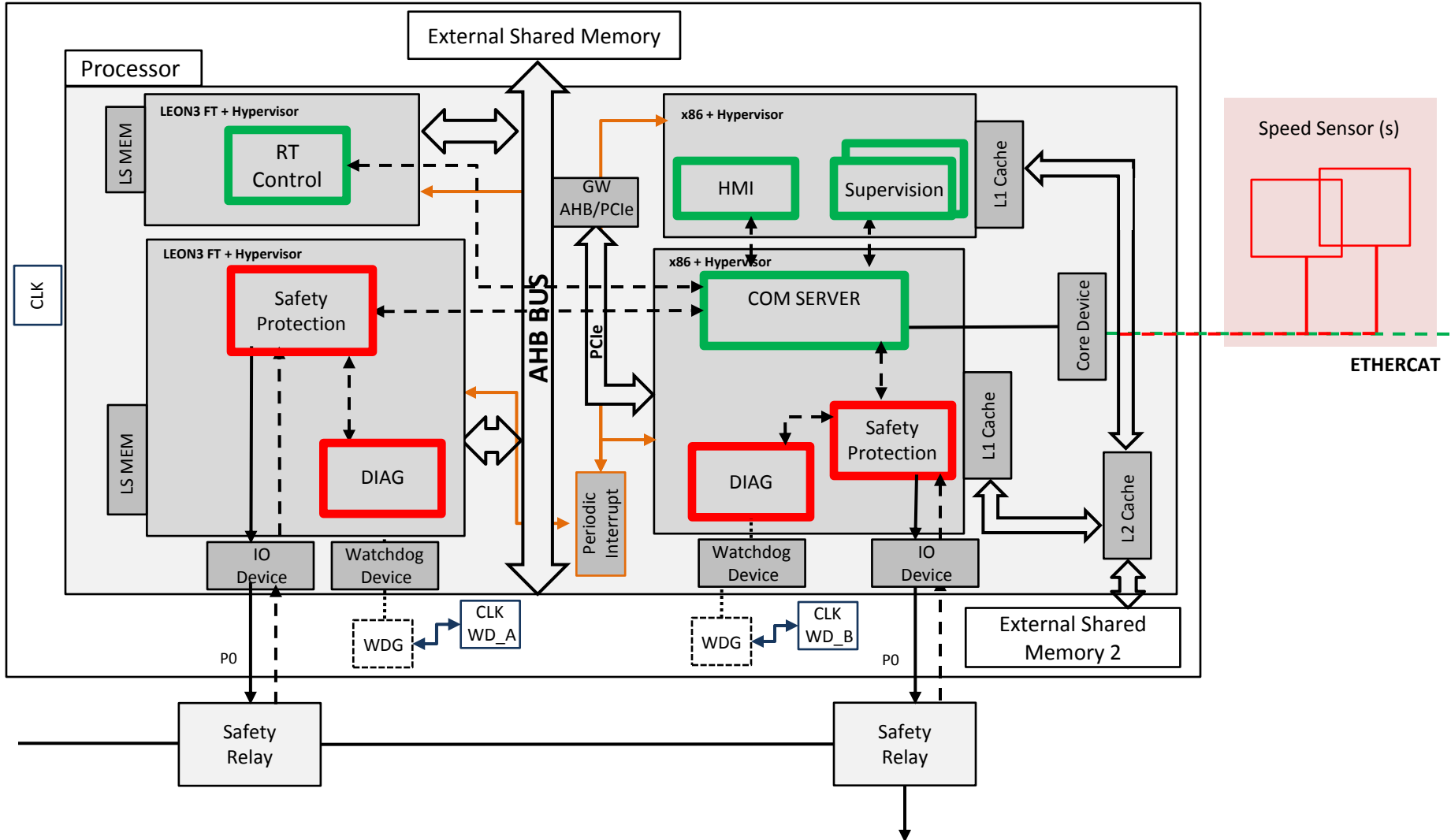


[1] Perez, J., et al. (2014). A safety concept for a wind power mixed-criticality embedded system based on multicore partitioning. Functional Safety in Industry Application, 11th International TÜV Rheinland Symposium, Cologne, Germany.

[2] Perez, J., et al. (2014). "A safety certification strategy for IEC-61508 compliant industrial mixed-criticality systems based on multicore partitioning." Euromicro DSD/SEAA Verona, Italy.

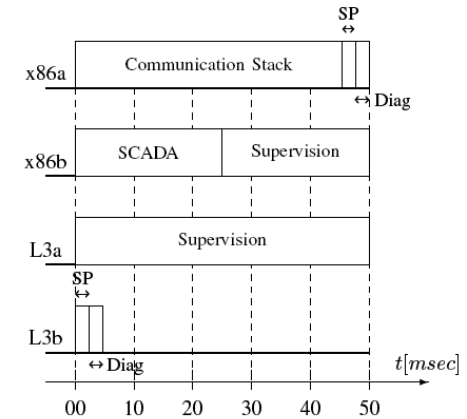
SAFETY CPU SINGLE PROCESSOR QUAD CORE PARTITIONED – 1oo2

SCPU



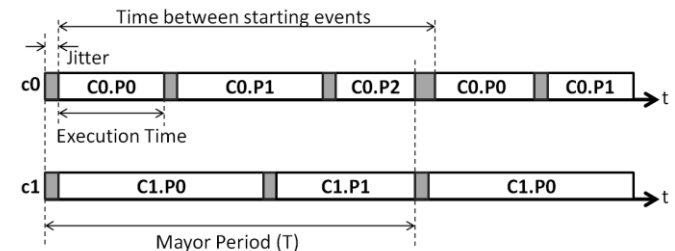
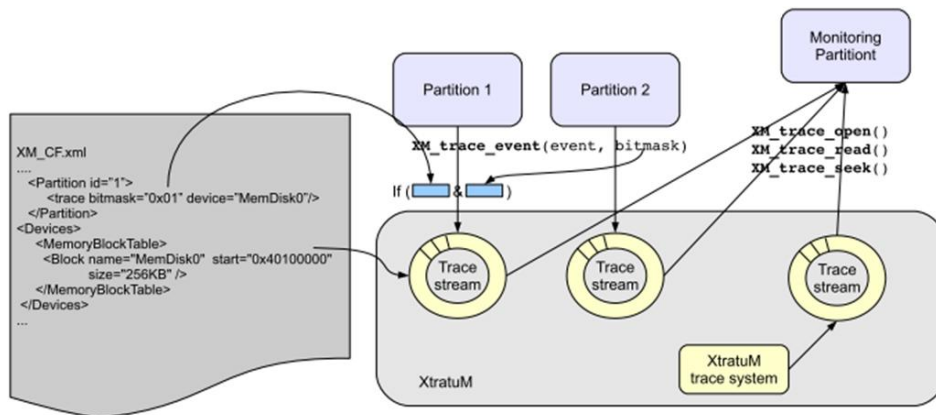
◇ Scheduling (IEC-61508-3 Annex E):

- Static cyclic scheduling algorithm.
- Pre-assigned guaranteed time slots defined at design
- Synchronized based on the global notion of time

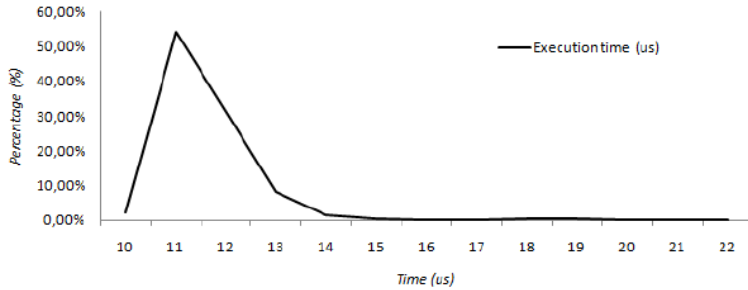


◇ Measurement Based Timing Analysis (MBTA):

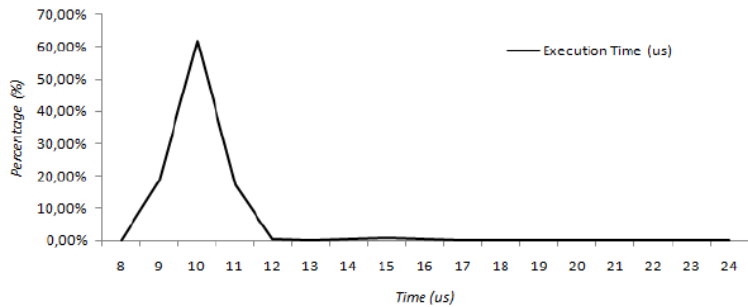
- Acquisition of run-time events using tracing support provided by hypervisor
- Definition and execution of worst case scenarios and error injection



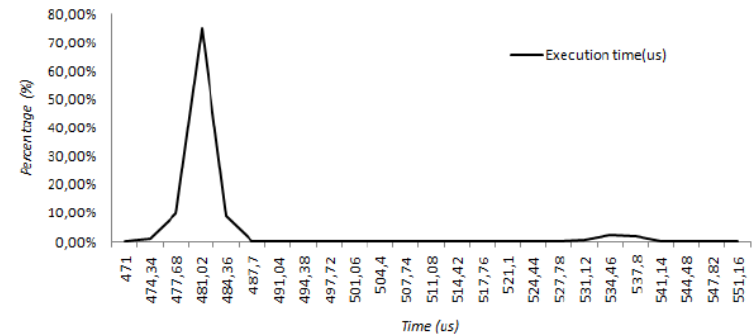
[1] Larrucea A. et al, “Temporal Independence Validation for IEC-61508 compliant Mixed-Criticality Systems based on Multicore Partitioning”, Forum on specification & Design Languages (FDL), 2015



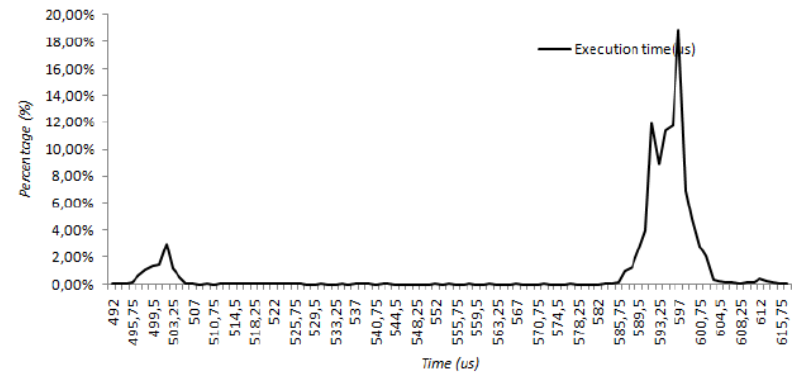
(a) Safety Protection partition on x86 processor.



(b) Supervision partition on x86 processor.



(a) Safety Protection partition on LEON3 processor.



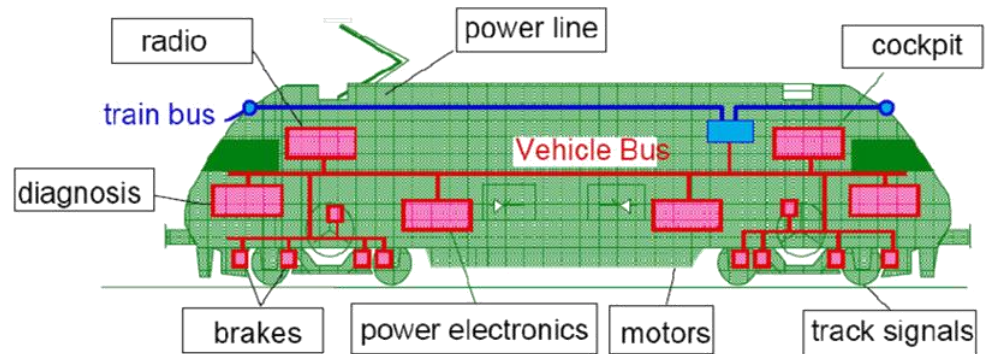
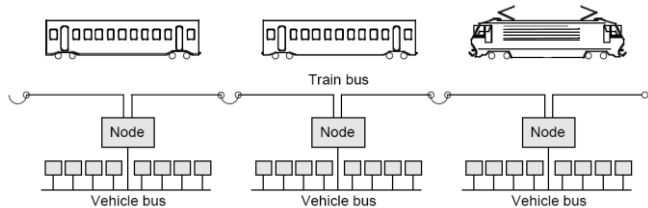
(b) Supervision partition on LEON3 processor.

Fig. 7. Execution Time measurements on x86 processor.

[1] Larrucea A. et al, "Temporal Independence Validation for IEC-61508 compliant Mixed-Criticality Systems based on Multicore Partitioning", Forum on specification & Design Languages (FDL), 2015

◇ (On-board) railway domain:

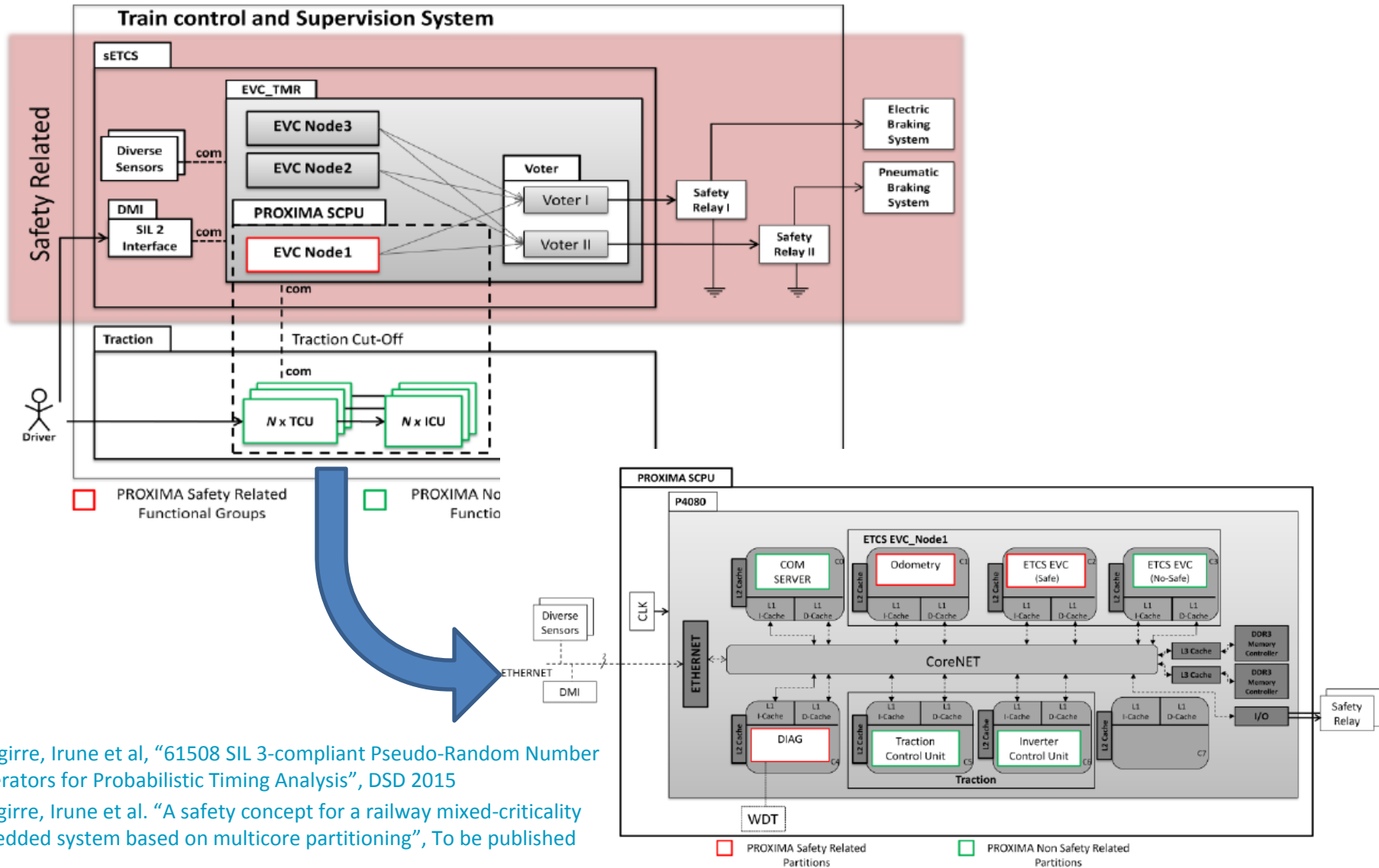
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- The number of ECUs (Electric Control Units) within a train system is of the order of a few hundred [2,3].
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[1] The European Rail Research Advisory Council (ERRAC), Joint Strategy for European Rail Research 2020.

[2] Kirrmann, H. and P. A. Zuber (2001). "The IEC/IEEE Train Communication Network." IEEE Micro vol. 21, no. 2: 81-92.

[3] F. Corbier, et al, *How Train Transportation Design Challenges can be addressed with Simulation-based Virtual Prototyping for Distributed Systems*, 3rd European congress Embedded Real Time Software (ERTS), France, 2006.

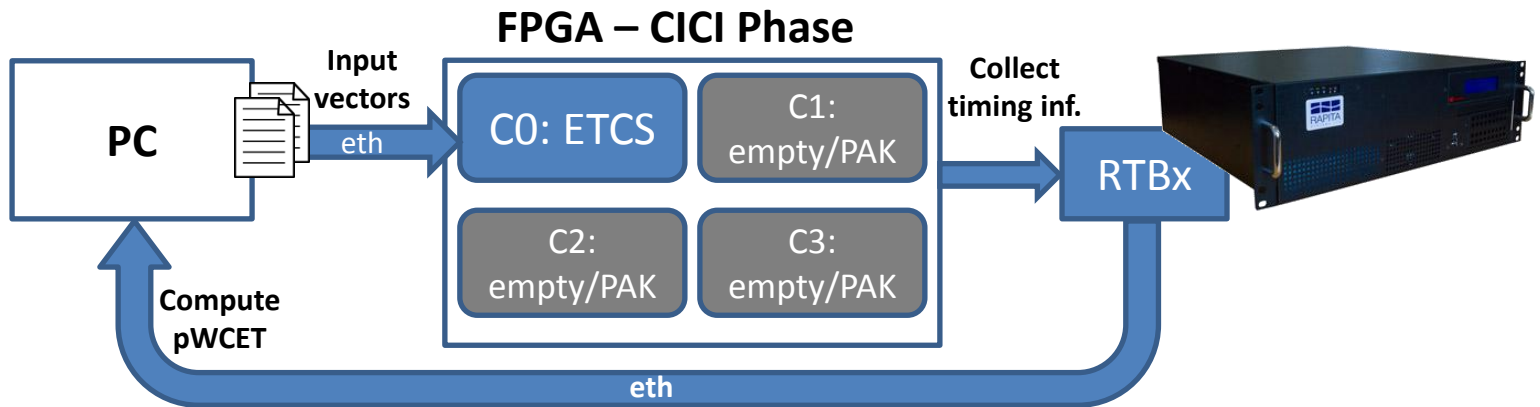


[1] Agirre, Irune et al, "61508 SIL 3-compliant Pseudo-Random Number Generators for Probabilistic Timing Analysis", DSD 2015

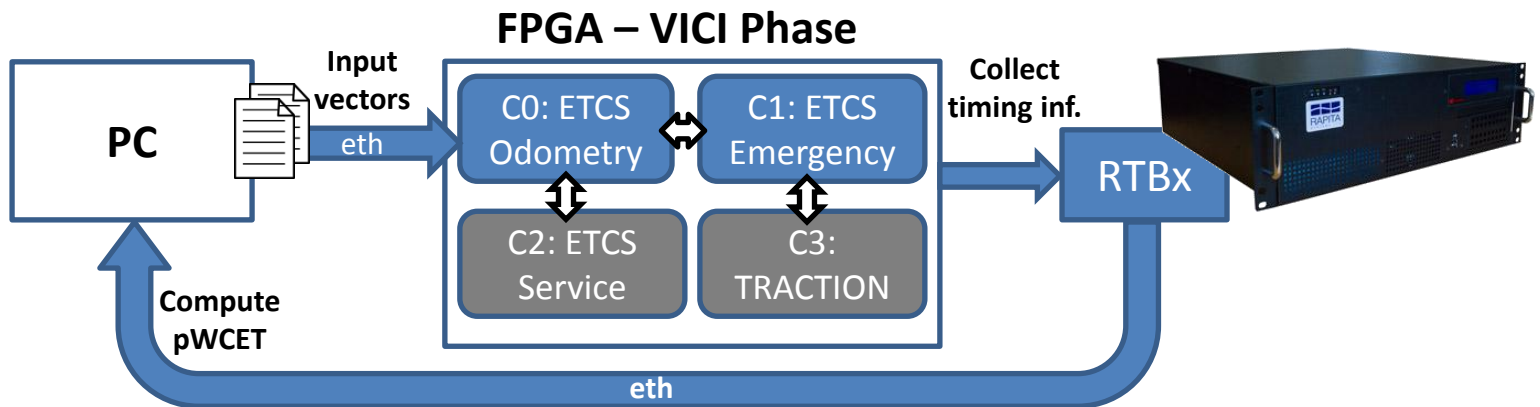
[2] Agirre, Irune et al. "A safety concept for a railway mixed-criticality embedded system based on multicore partitioning", To be published

○ Railway Case study Experiments:

CICI phase:



VICI phase:



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05

Conclusions and lessons learnt

- ◇ It is feasible to achieve SIL3 IEC-61508 / PId ISO-13849 / SILX EN-50128 in research 'case studies' with current safety standard versions using:
 - ◇ COTS multicore
 - ◇ Partitioning with hypervisor
 - ◇ WCET estimation based on MBTA and PTA
- ◇ There is a need and opportunity for WCET, but consider industry & research worlds:
 - ◇ Common understanding (e.g., fail safe, temporal isolation vs. Temporal independence)
 - ◇ Complexity management
 - ◇ Product and safety certification context
 - ◇ Product life-cycle and variability
- ◇ The same strategy can be extended to different domains with safety standards that use IEC-61508 as reference standard.
 - ✓ Wind Turbine, IEC-61508 SIL3 and ISO-13849 PId.
 - ✓ Railway signaling, SIL4 EN-5012X
 - ◇ Working with automotive domain case study ASILC ISO-26262.





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