

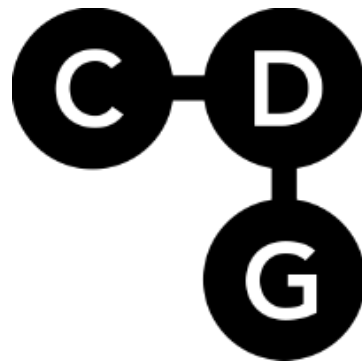
Timing Analysis of Event-Driven Programs with Directed Testing

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Talk Outline

- **Introduction**

- Toyota UA Case
- Problem Definition and Previous Work
- Review of Classic Directed Testing
- Motivating Experiments

- **Our Approach**

- Example
- VICE: Algorithms and tools

- **Experiment Results**

- **Conclusion**

- *Event-Based Directed Testing* improves the state-of-art

Aug. 28, 2009, San Diego CA, USA

- Toyota Lexus ES 350 sedan
 - UA Reached 100 mph+
- 911 Emergency Phone Call from passenger during event
 - All 4 occupants killed in crash

- Driver:

Mark Saylor, 45 year old male.

Off-duty California Highway Patrol Officer; vehicle inspector.

- Crash was blamed on wrong floor mats causing pedal entrapment
- Brake rotor damage indicated “endured braking”
- This event triggered escalation of investigations dating back to 2002 MY

The New York Times

February 1, 2010



The wreckage of a Lexus ES 350 in which four people died in August after it accelerated out of control.

Gomez Lav Firm

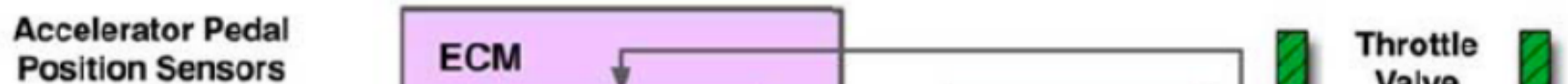
Recalls & Public Discussion

(Brakes might not mitigate open throttle – more later)

- **Floor mat recalls**
 - Sept. 2007 recall to fasten floor mats
 - Wider recall Oct./Nov. 2009 after Saylor mishap
- **Sticky gas pedal recall**
 - Jan. 2010 and onward
- **Congressional investigation**
 - Toyota President testifies to US Congress, Feb. 2010
 - April 2010: Economic loss class action venue selected

NASA Investigation

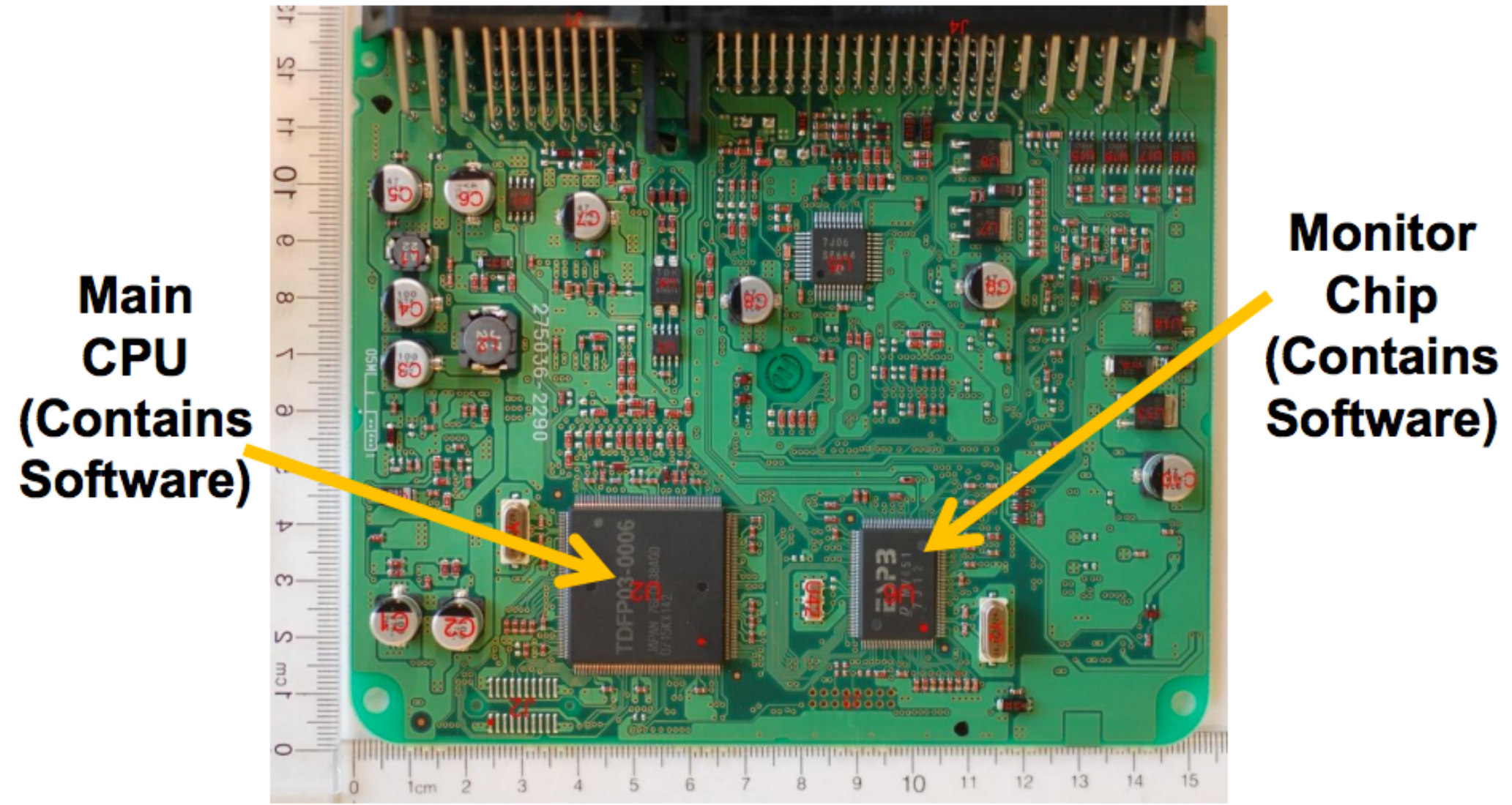
- NASA team investigates UA (2010-2011)
 - Including **Electronic Throttle Control System (ETCS)**
 - Controls air + fuel + spark → engine power



- **Timing analysis too difficult for NASA**
 - **Worst Case Execution Time** difficult due to busy-wait loops, indirect recursion, etc. [NASA App. A pp. 120-133]
 - But, no deadline misses seen [NASA App. A. pp. 120-125]



Toyota 2008 ETCS – Two CPUs



http://m.eet.com/media/1201063/Toyota_ECM.jpg

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\$1.6B Economic Loss Class Action

- “Lawsuit pursues claims for breach of warranties, unjust enrichment, and violations of various state consumer protection statutes, among other claims.”
 - <https://www.toyotaelsettlement.com/>
 - 2002 through 2010 models of Toyota vehicles
 - Toyota denies claims; settled for \$1.6 Billion in Dec. 2012
 - Brake override firmware update for in some **recent** models

Please be advised that the Brake Override System installation is now available for the following make and model vehicles:

2008 - 2010 Toyota LandCruiser

2009 - 2010 Toyota Corolla

2009 - 2010 Toyota Corolla Matrix

2008 - 2010 Toyota Highlander

2006 - 2010 Toyota RAV4

2003 - 2009 Toyota 4Runner

2007 - 2010 Toyota Tundra

2008 - 2010 Lexus LX

2010 Lexus RX

<https://www.toyotaelsettlement.com/>
3 August 2014

Bookout/Schwarz Trial

- October 2013, Oklahoma
 - Fatal 2007 crash of a 2005 Toyota Camry
 - **Neither floor mat nor sticky pedal recalls cover this MY; no “fixes” announced**
- Toyota blamed driver error for crash
 - Mr. Arora (Exponent) testified as Toyota software expert
 - “[Toyota’s counsel] theorized that **Bookout mistakenly pumped the gas pedal instead of the brake**, and by the time she realized her mistake and pressed the brake, it was too late to avoid the crash” [<http://bigstory.ap.org/article/oklahoma-jury-considers-toyota-acceleration-case>]
- Plaintiffs blamed ETCS
 - Dr. Koopman & Mr. Barr testified as software experts
 - Testified about **defective safety architecture & software defects**
 - **150 feet of skid marks** implied open throttle while braking



[<http://money.cnn.com/2013/10/25/news/companies/toyota-crash-verdict/>]

WCET

- Controllers in safety time-critical embedded systems are expected to finish their tasks within reliable time bounds.
 - Underestimation causes missing deadlines and leads to bugs
 - Overestimation wastes process availability.
- *Question: what is the exact WCET across all inputs?*
 1. Program P , K is the WCET of all executions of P , if P 's WCET never grows beyond K .
 2. There is a possible schedule of events and an execution of the program P such that the WCET becomes K .

WCET in Literature

– Dynamic Analysis

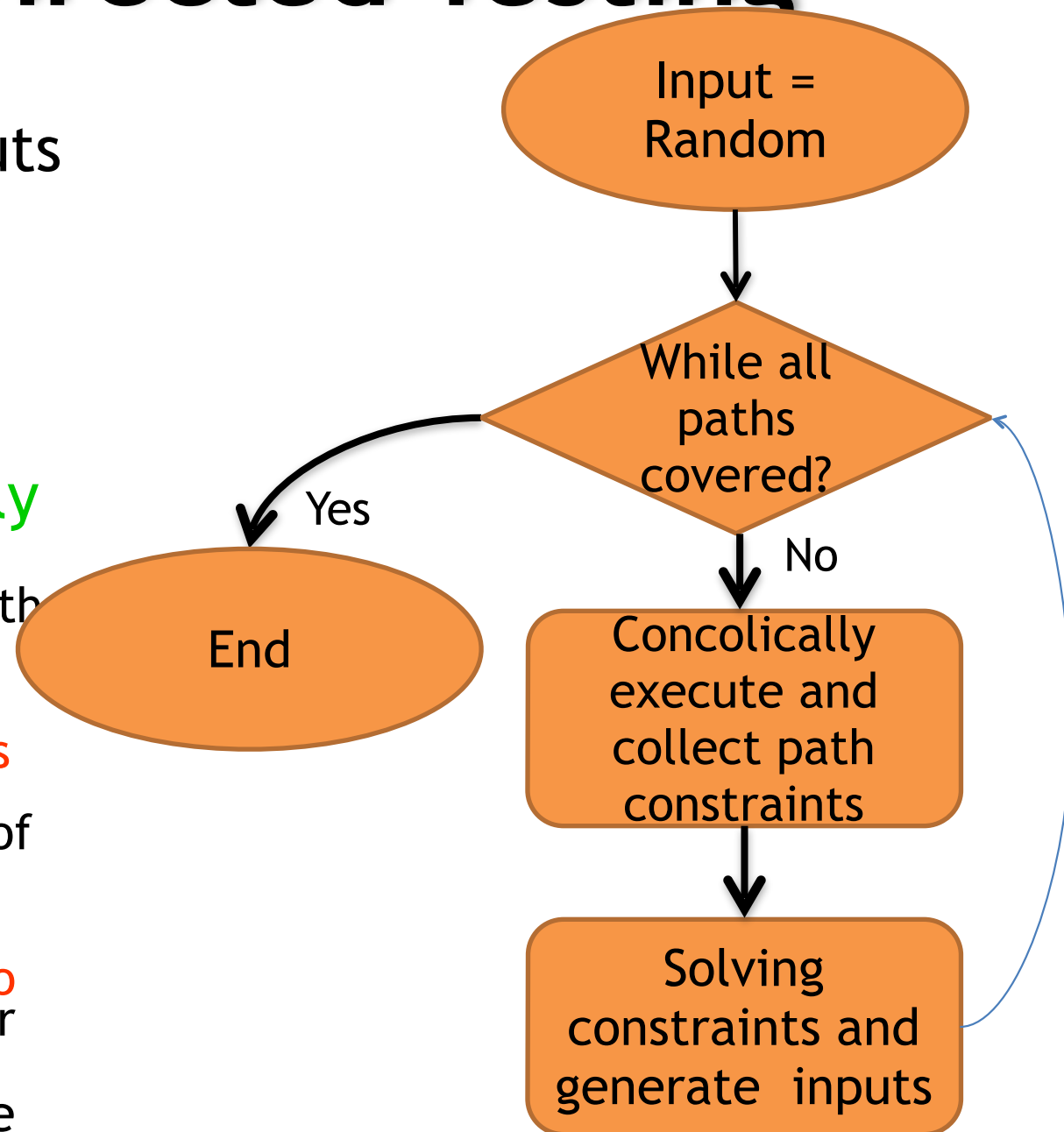
- Random Algorithms:
 - [Bernat et. Al., *RTSS'02*]
- Genetic Algorithm:
 - [Atanassov et. Al., *EWDC'01*]
- Classic Directed Testing:
 - [N. Williams and M. Roger, *AST'09*]

– Static Analysis

- [Holsti et. Al., *ESA'2000*]
- [C.Ferdinand, *BIS'04*]
- [Gustafsson and Ermedahl, *RTSS'06*]

Classic Directed Testing

- Generate concrete inputs one by one
 - each input leads program along a different path
- On each input execute program both **concretely** and **symbolically**
 - concrete execution **guides** the symbolic execution
 - concrete execution **enables** symbolic execution to overcome incompleteness of theorem prover
 - symbolic execution **helps to generate** concrete input for next execution
 - increases coverage

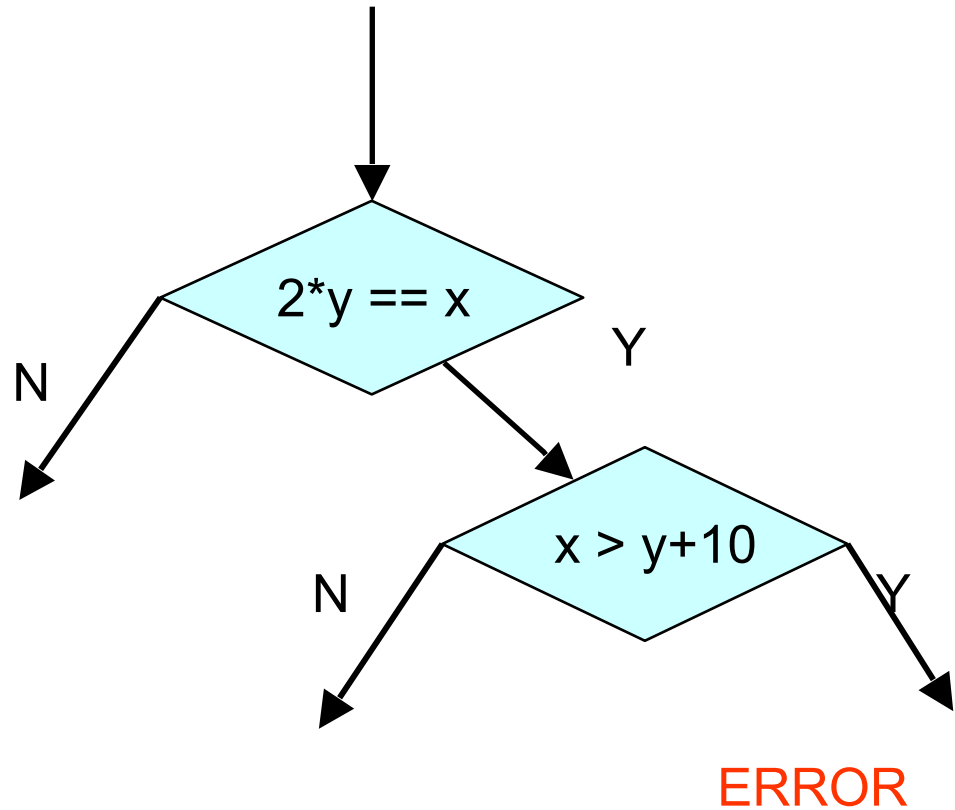


Example

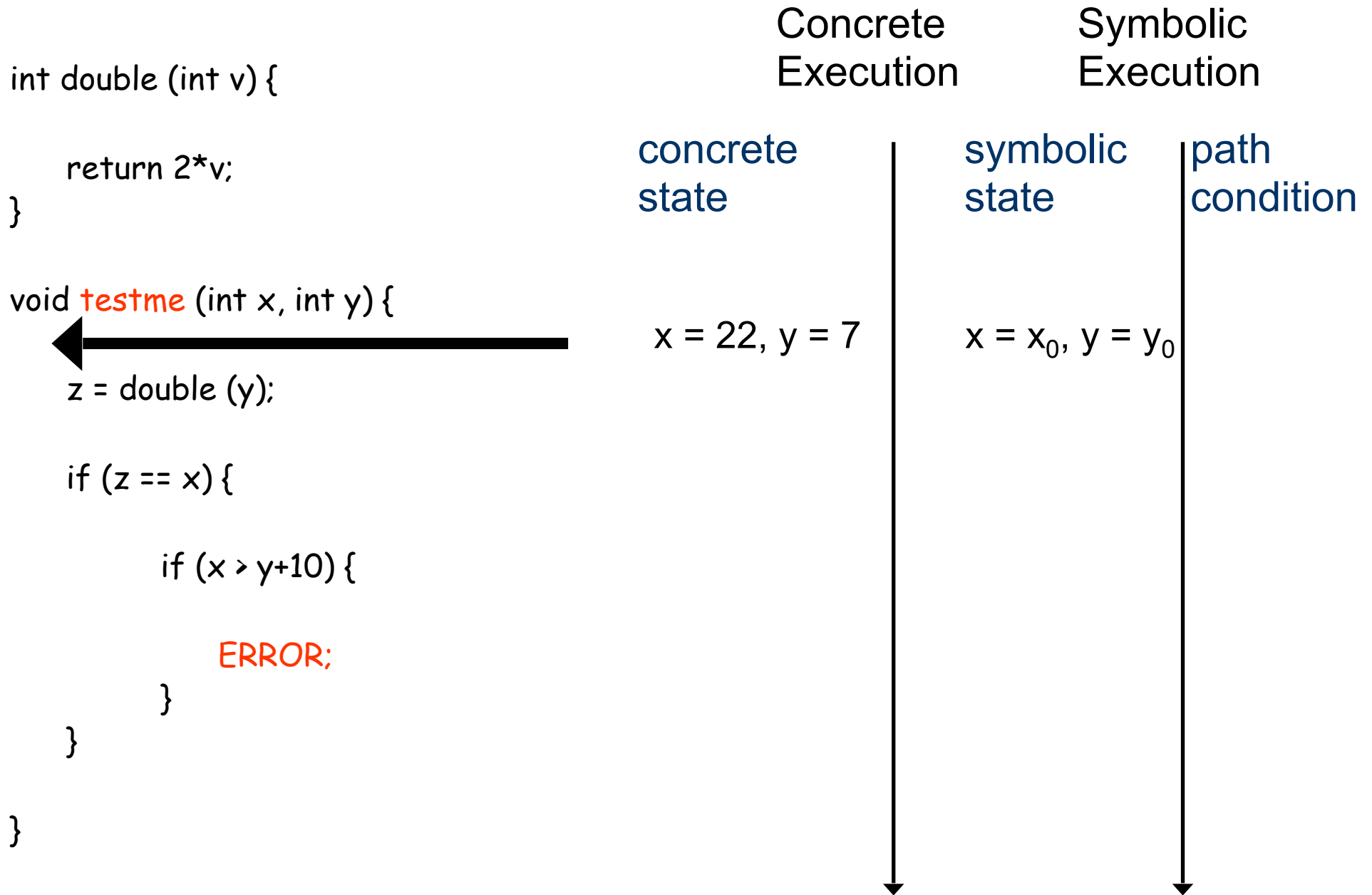
```
int double (int v) {  
    return 2*v;  
}  
  
void testme (int x, int y) {  
    z = double (y);  
    if (z == x) {  
        if (x > y+10) {  
            ERROR;  
        }  
    }  
}
```

Example

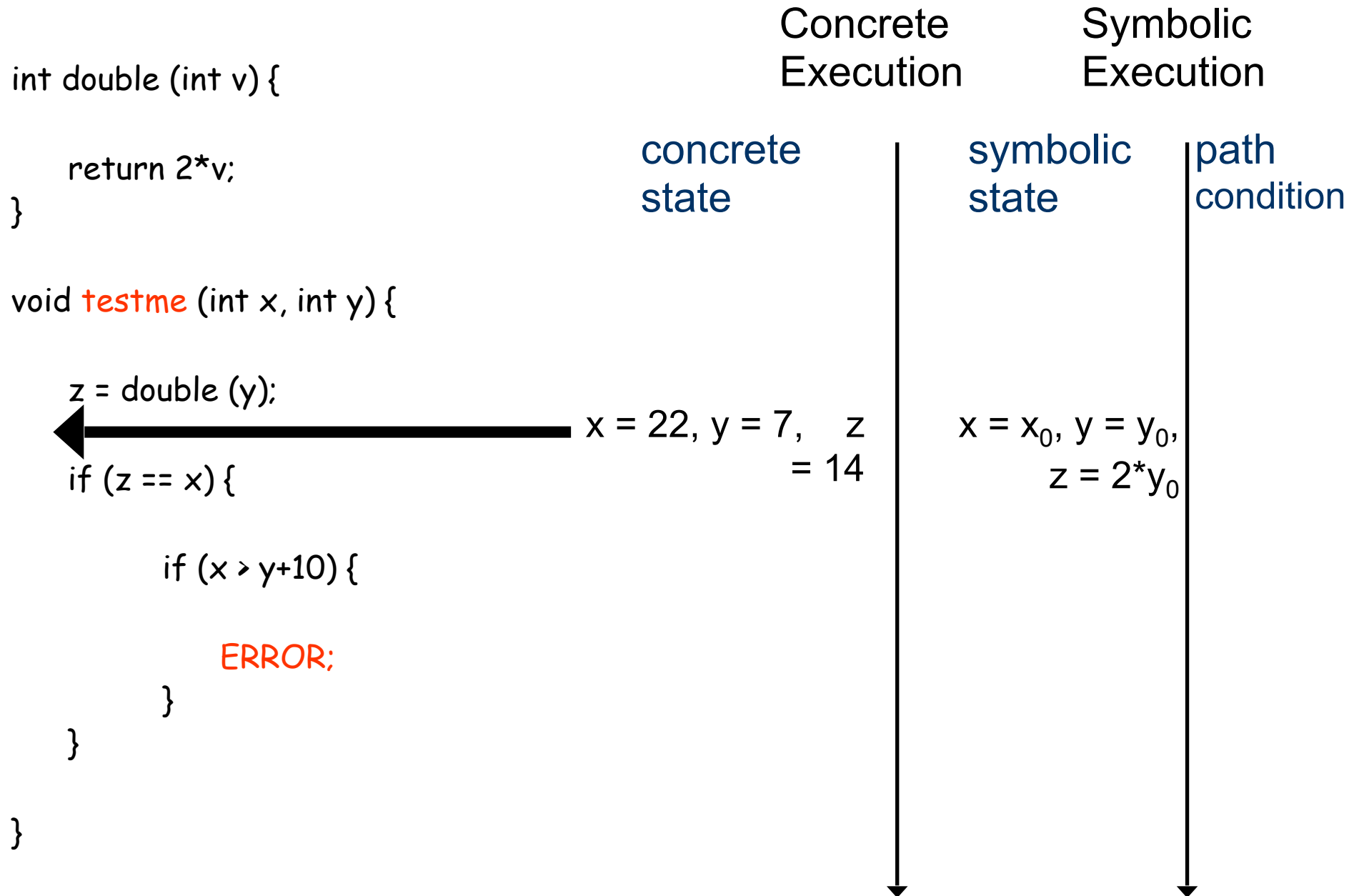
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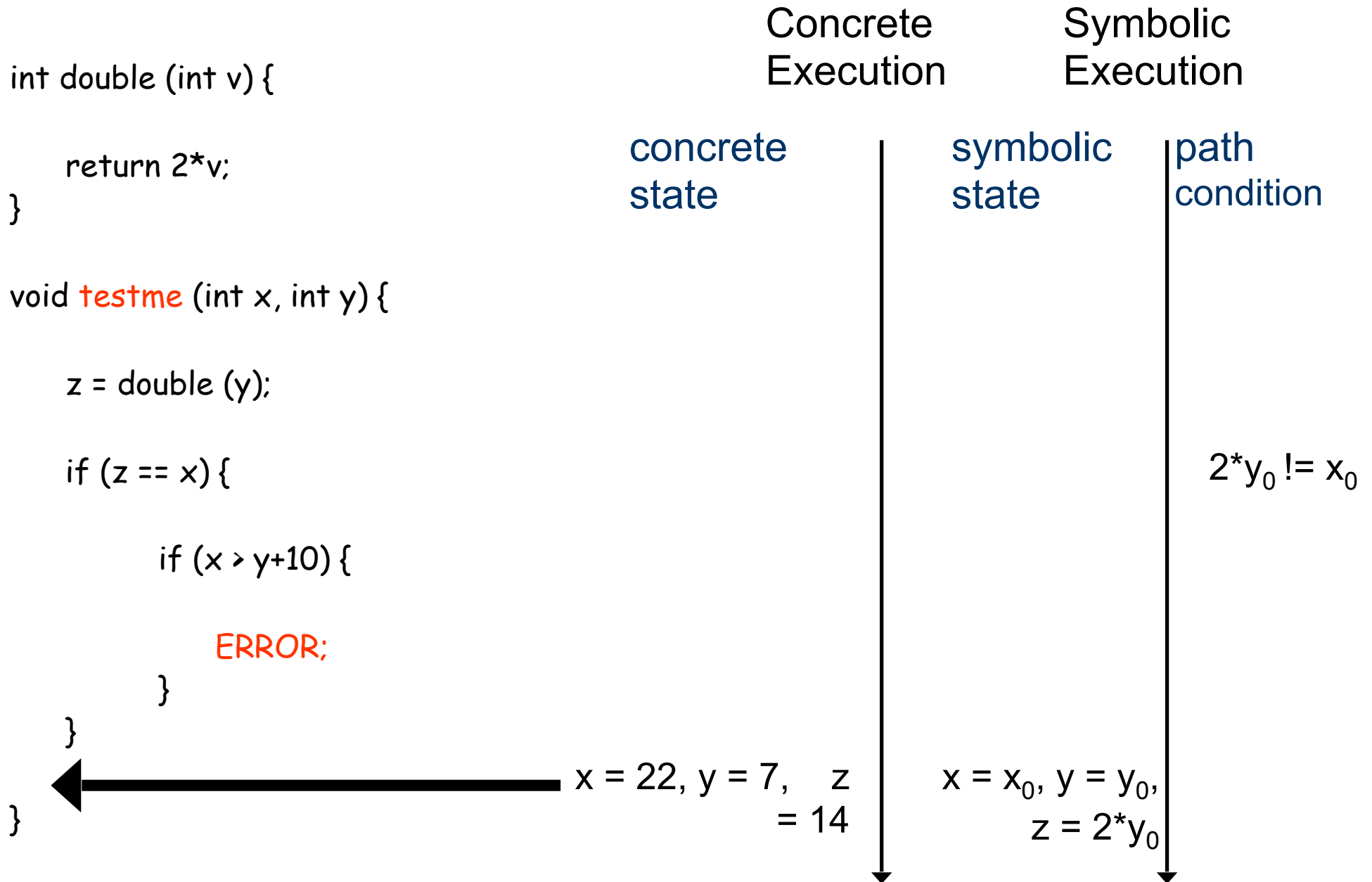
Directed Testing Approach



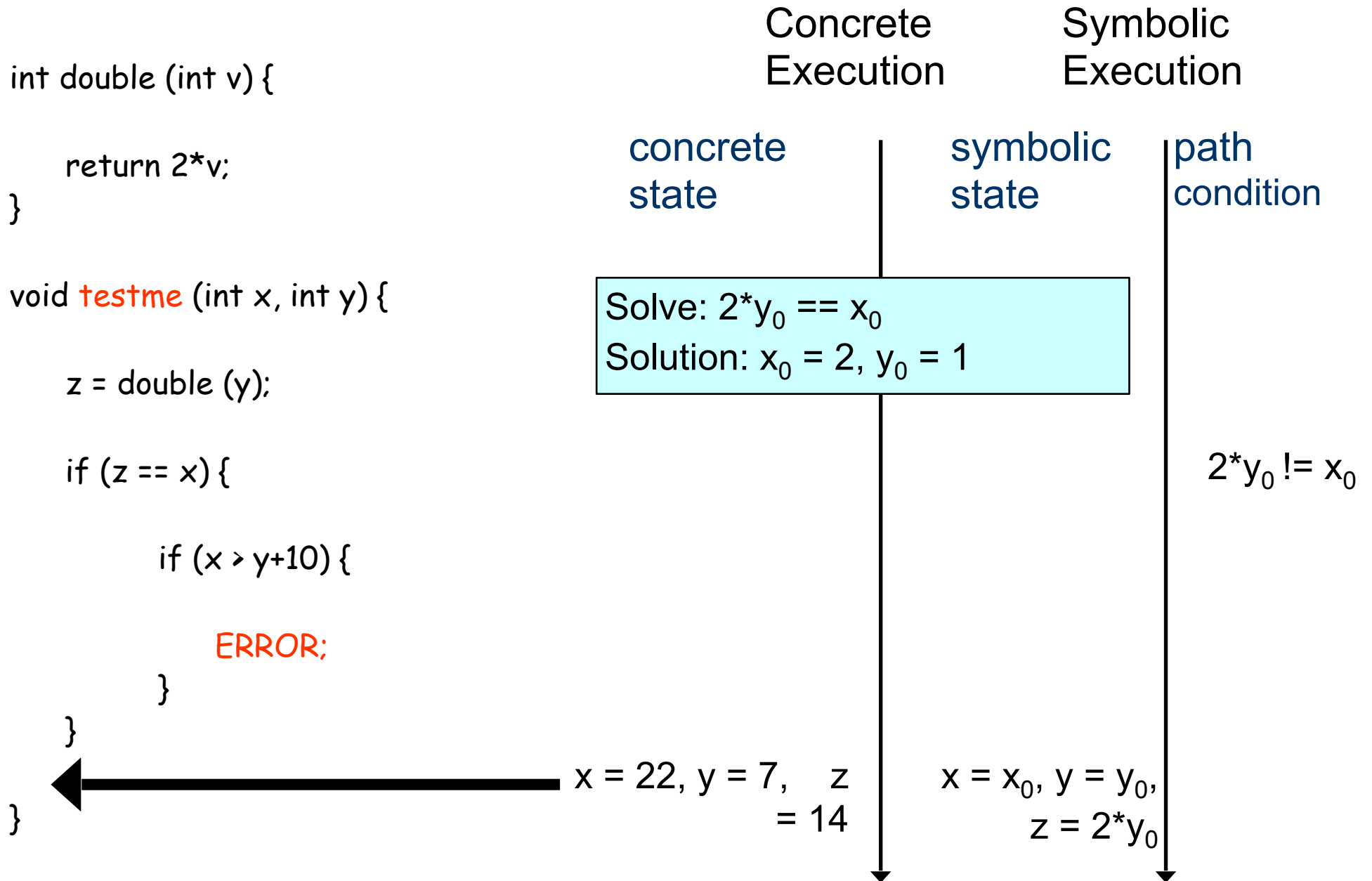
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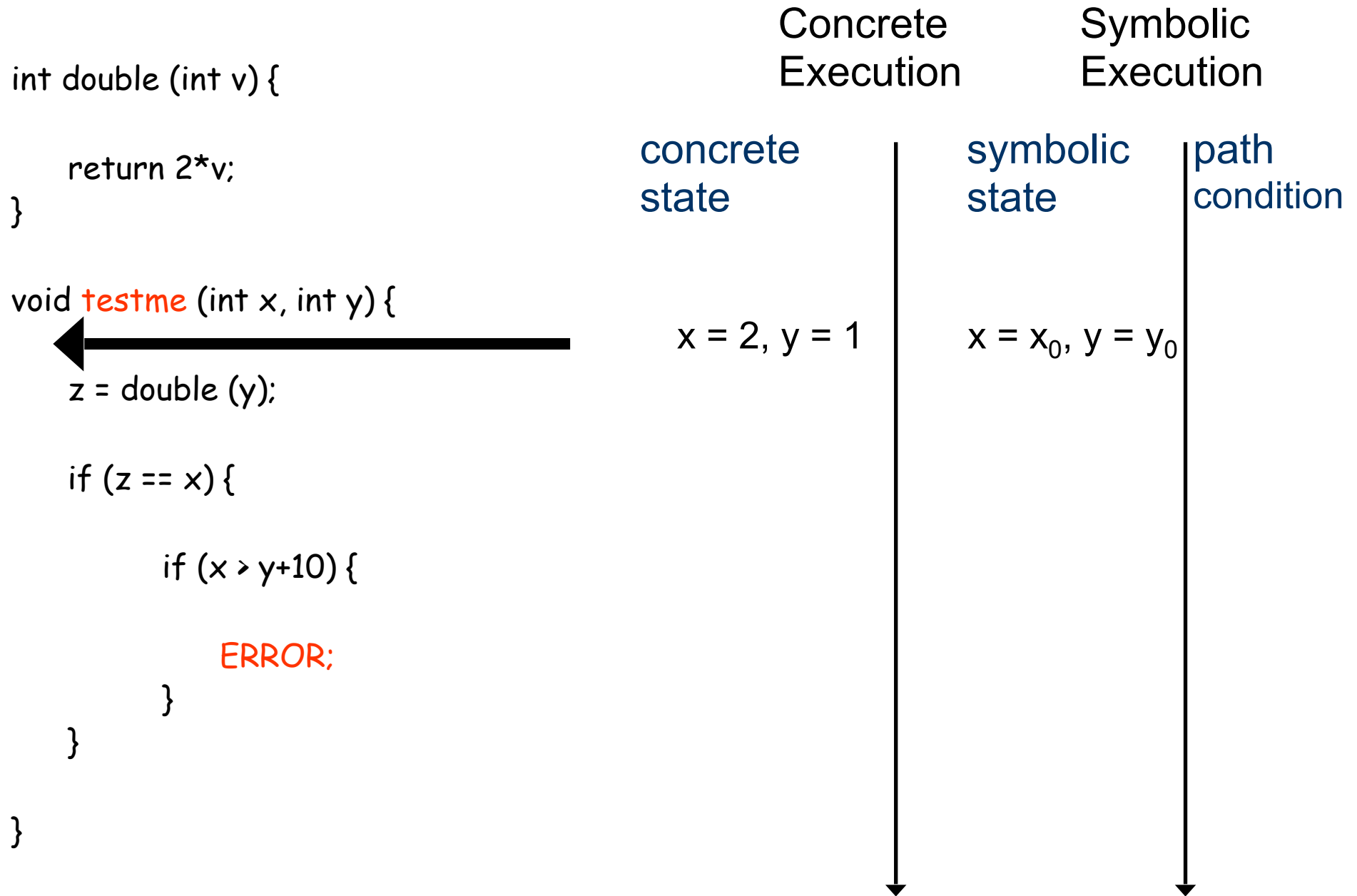
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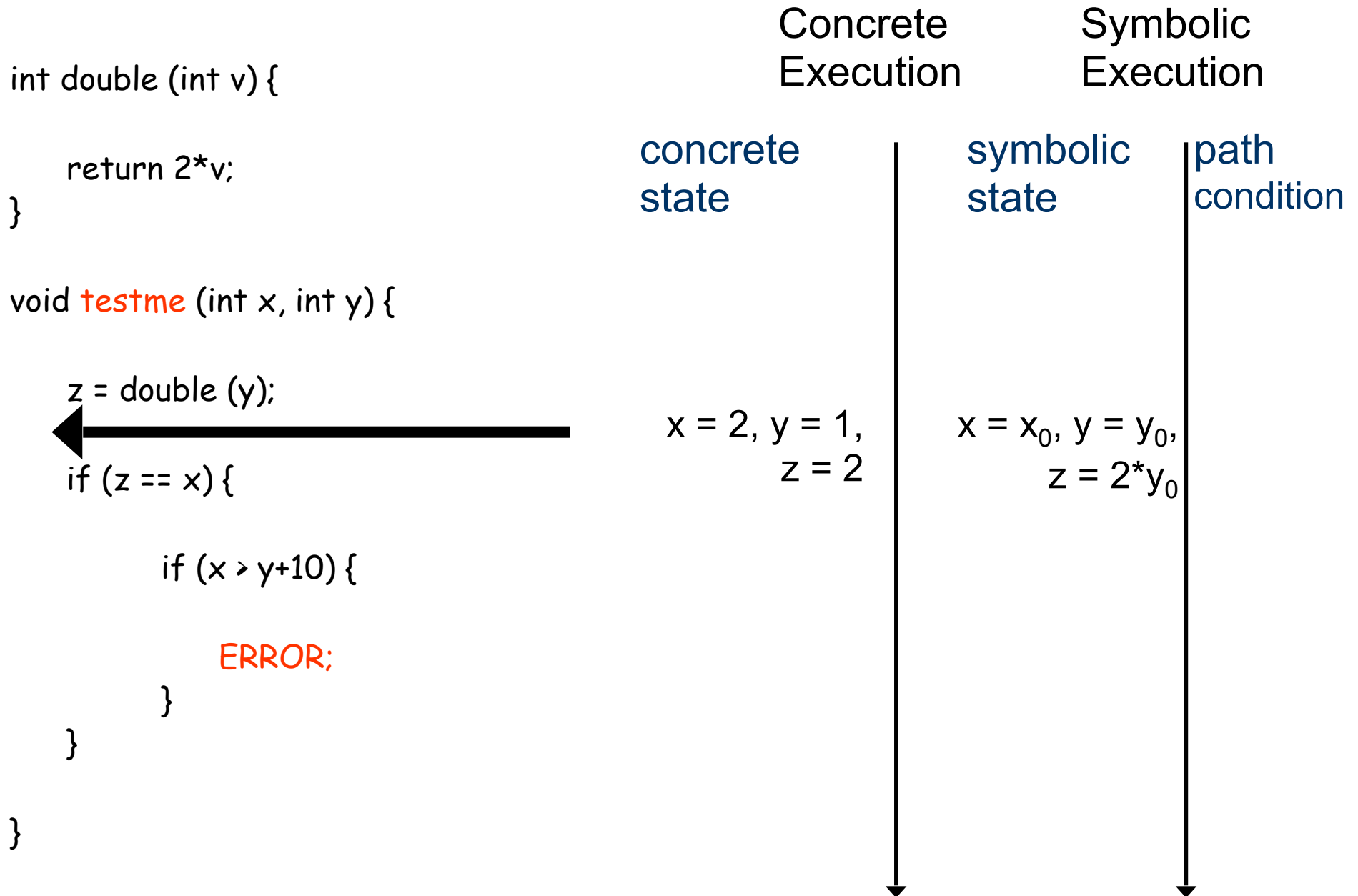
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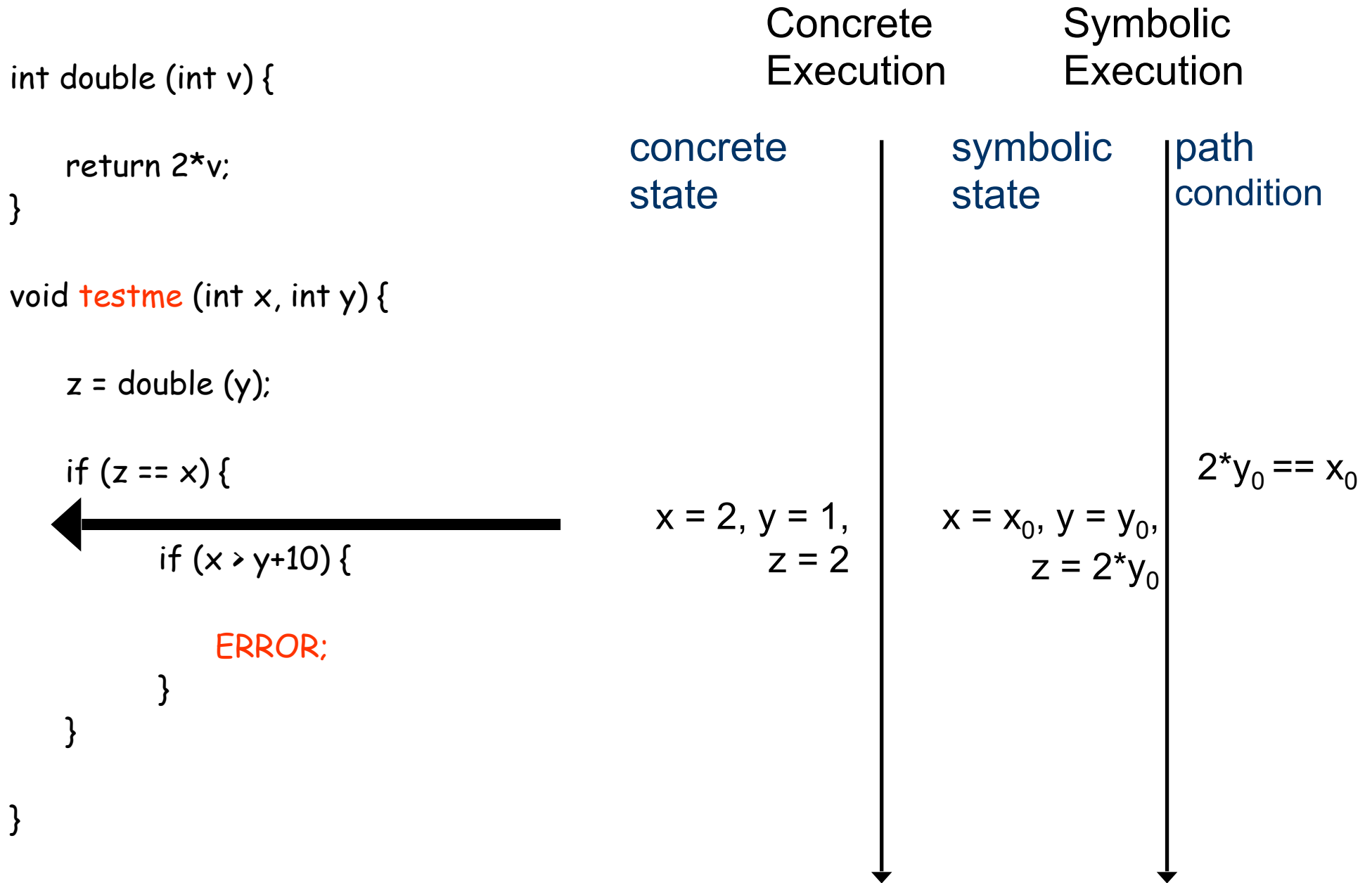
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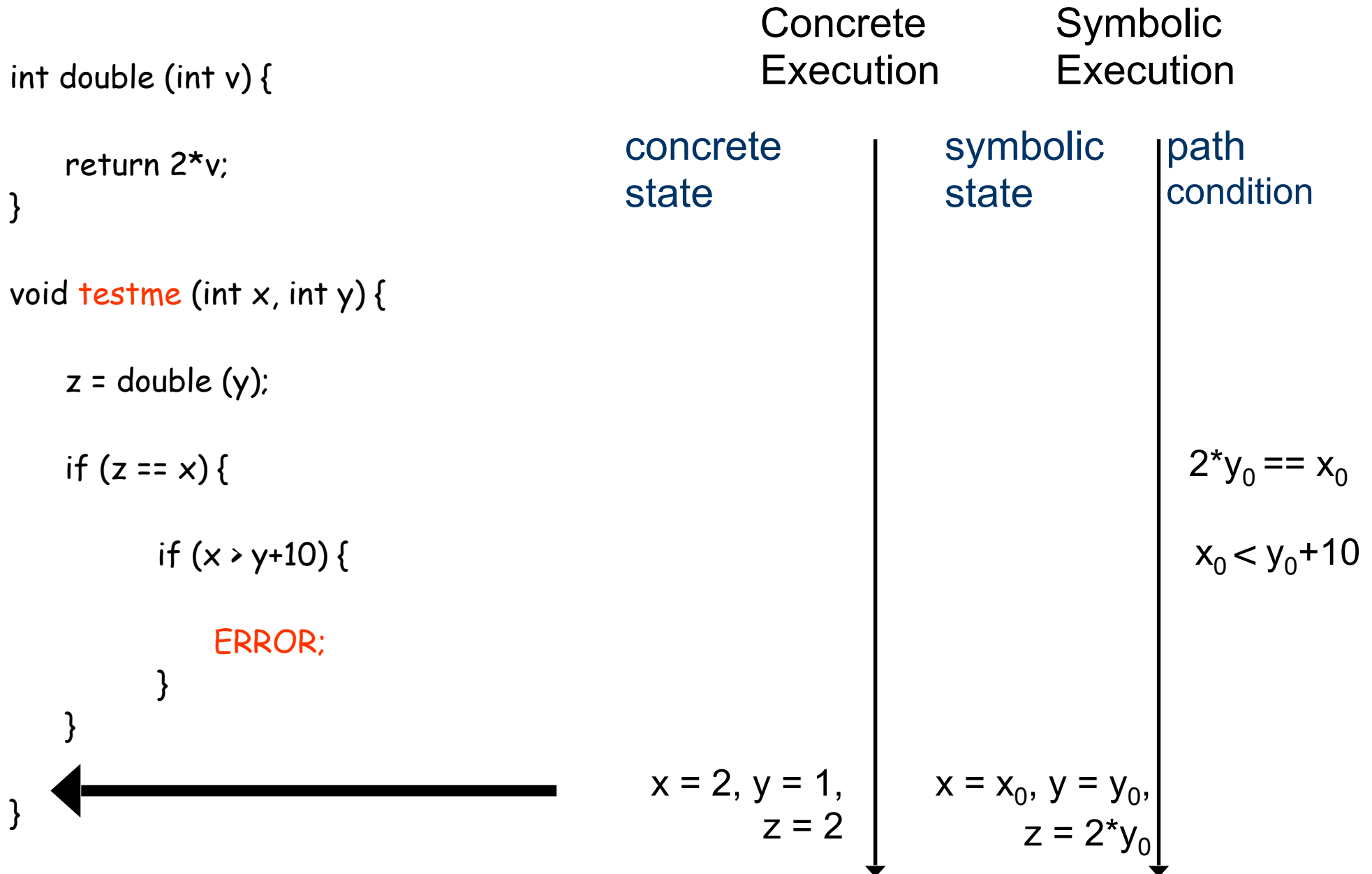
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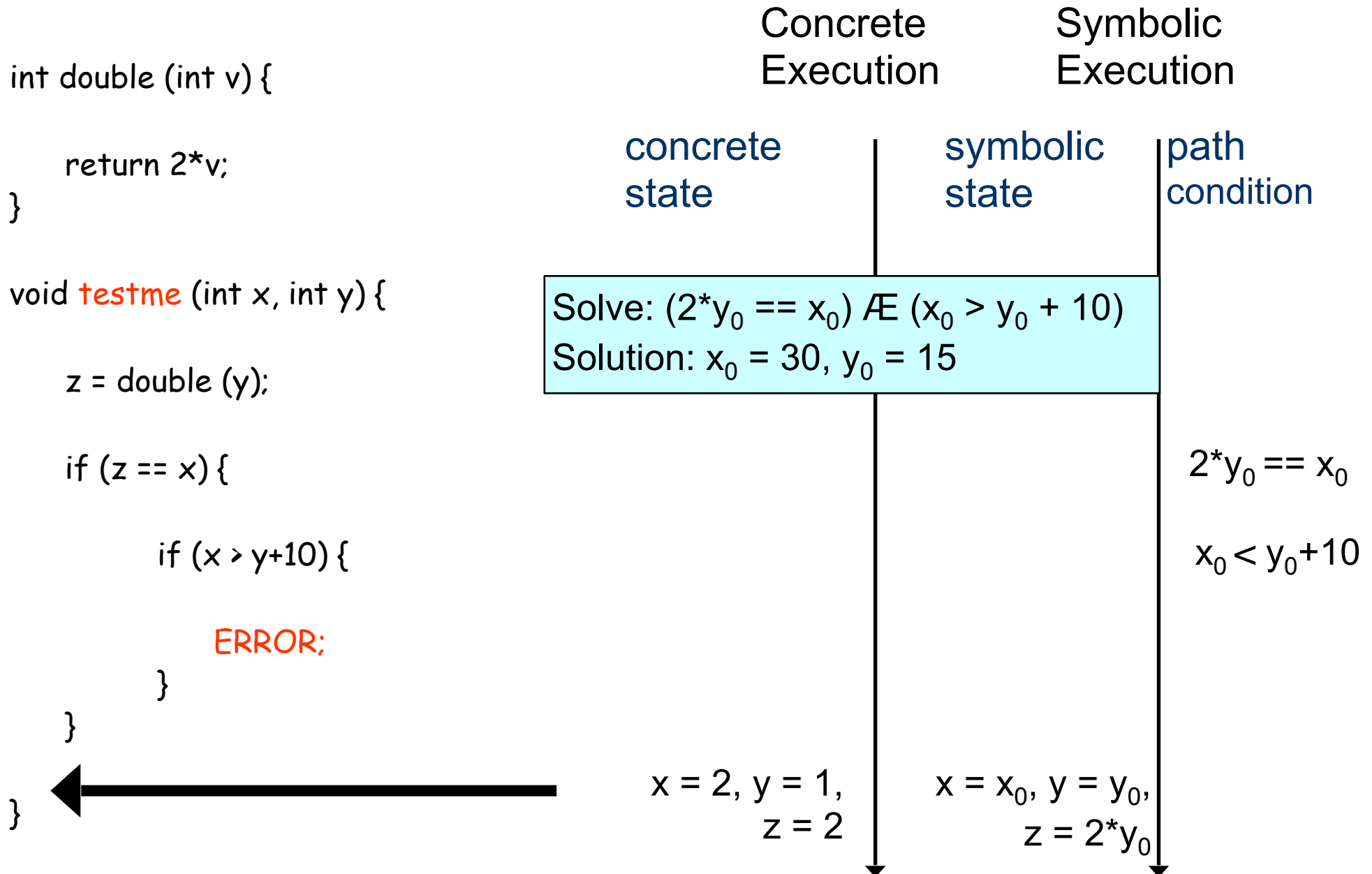
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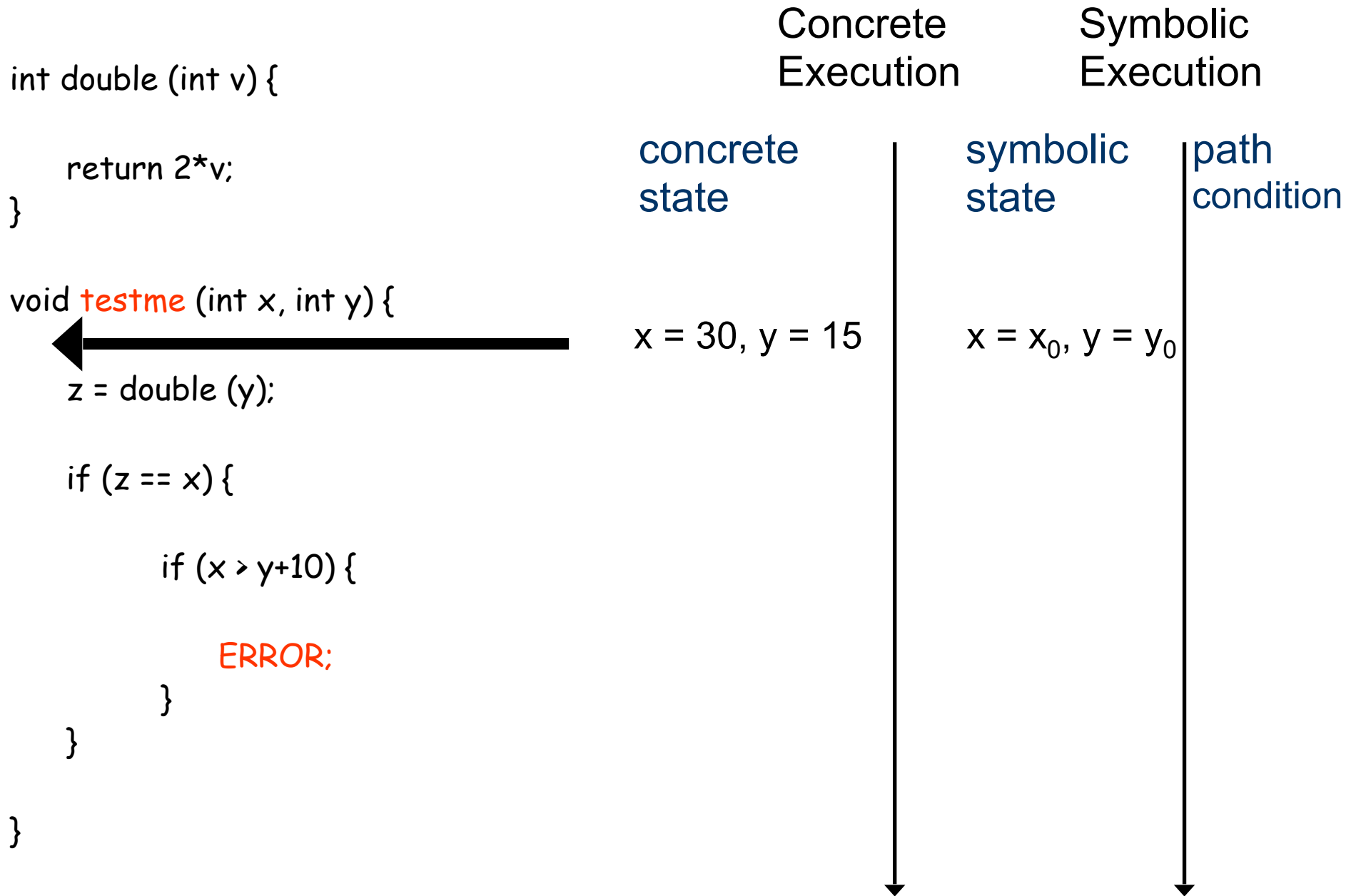
Directed Testing Approach



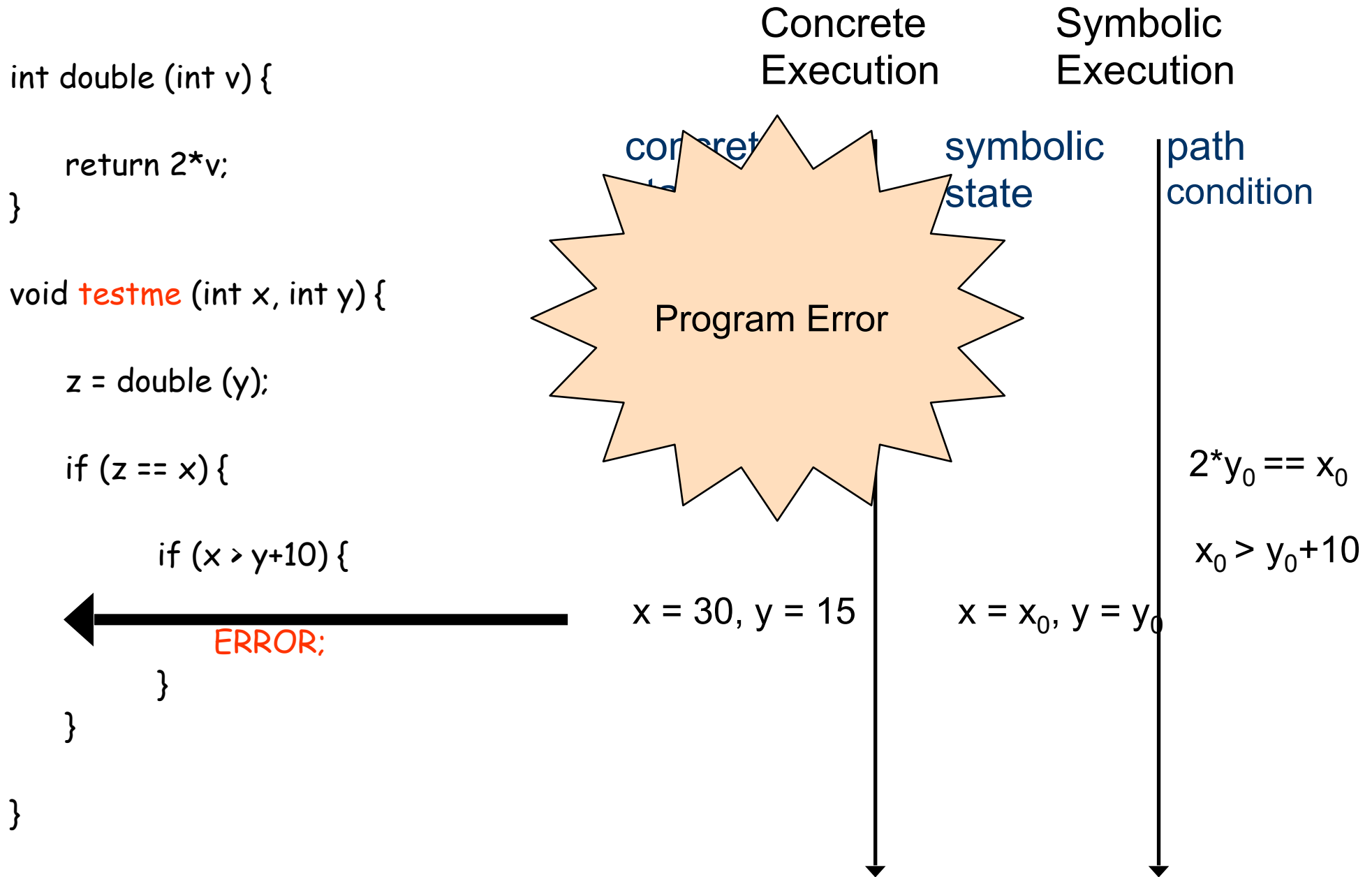
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Directed Testing Approach

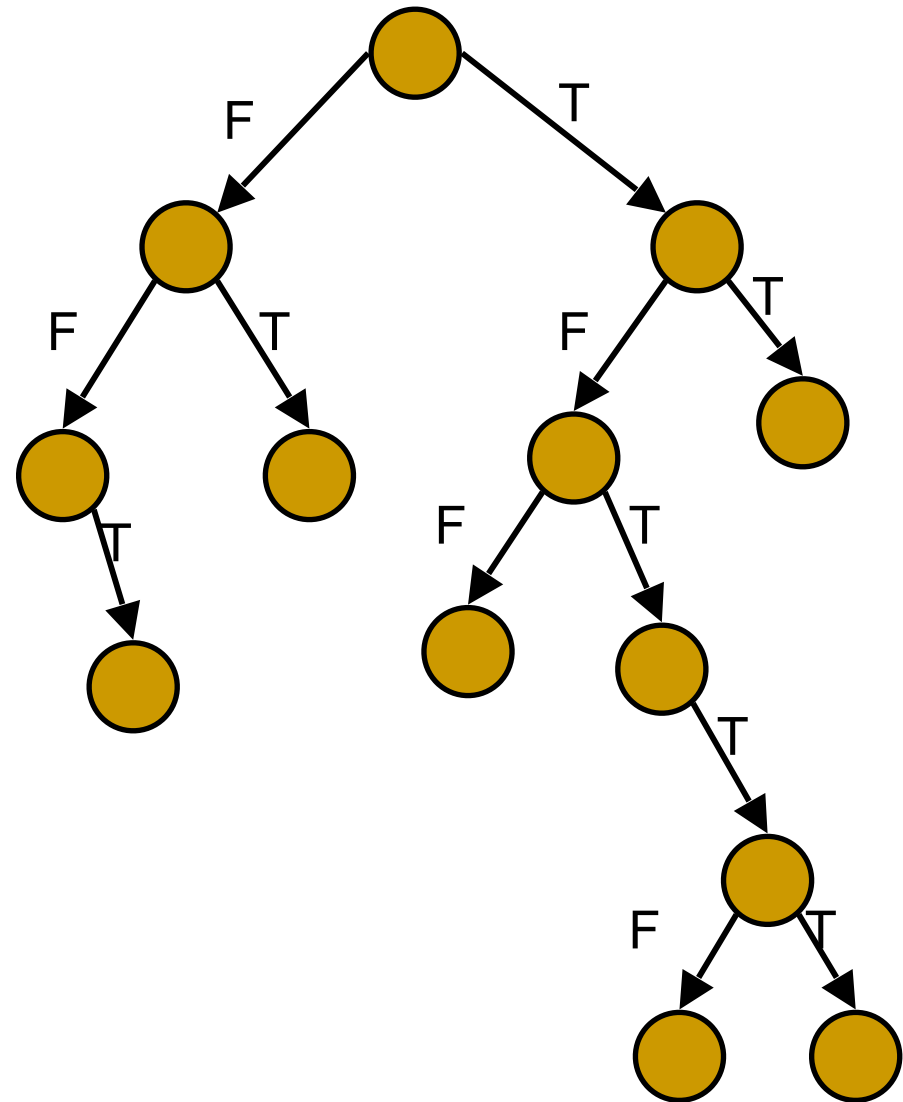


Directed Testing Approach



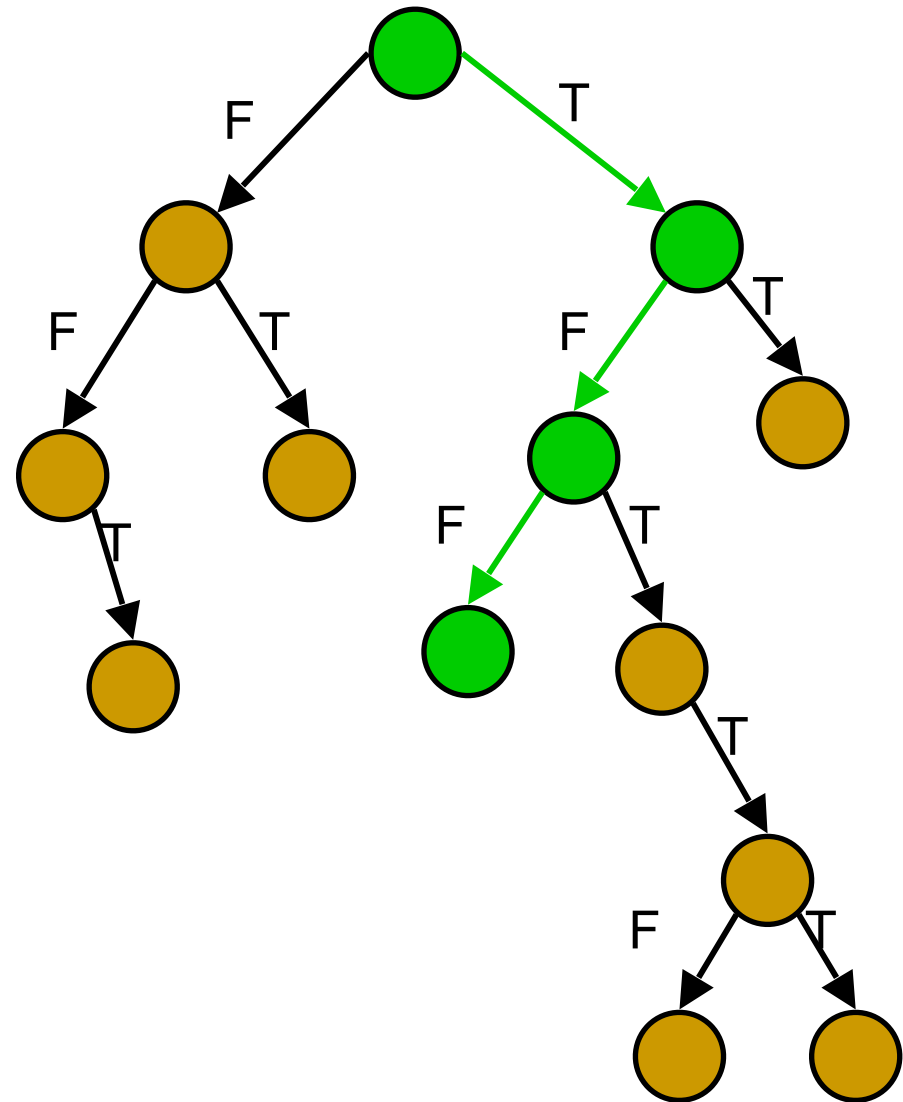
Explicit Path Model Checking

- Traverse all execution paths one by one to detect errors
 - assertion violations
 - program crash
 - uncaught exceptions



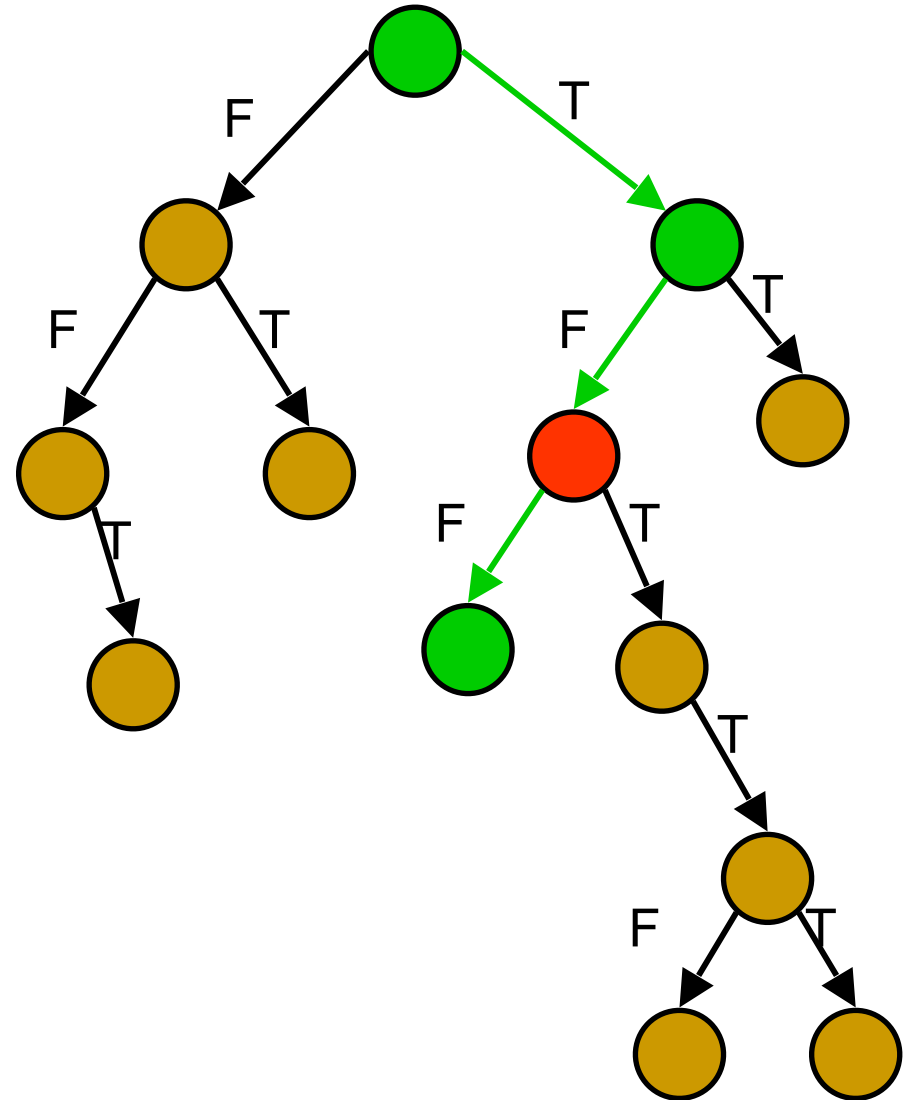
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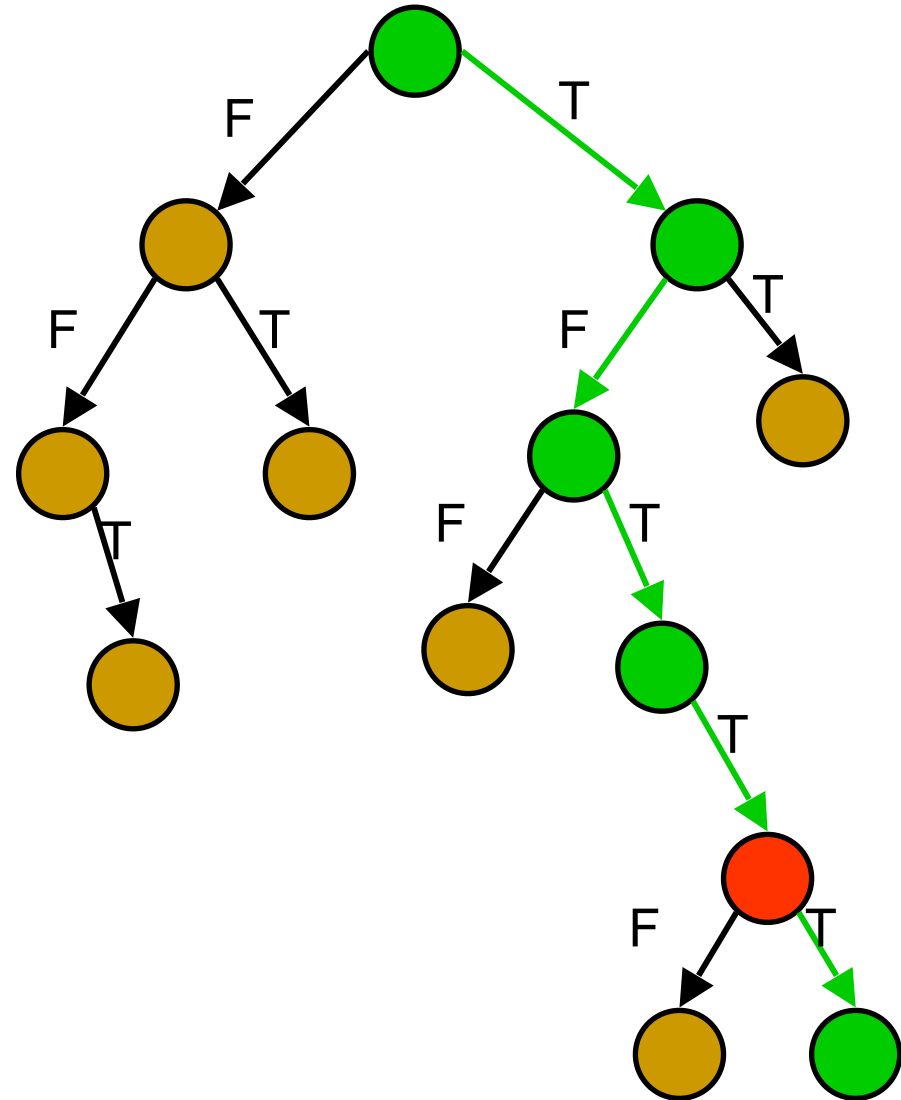
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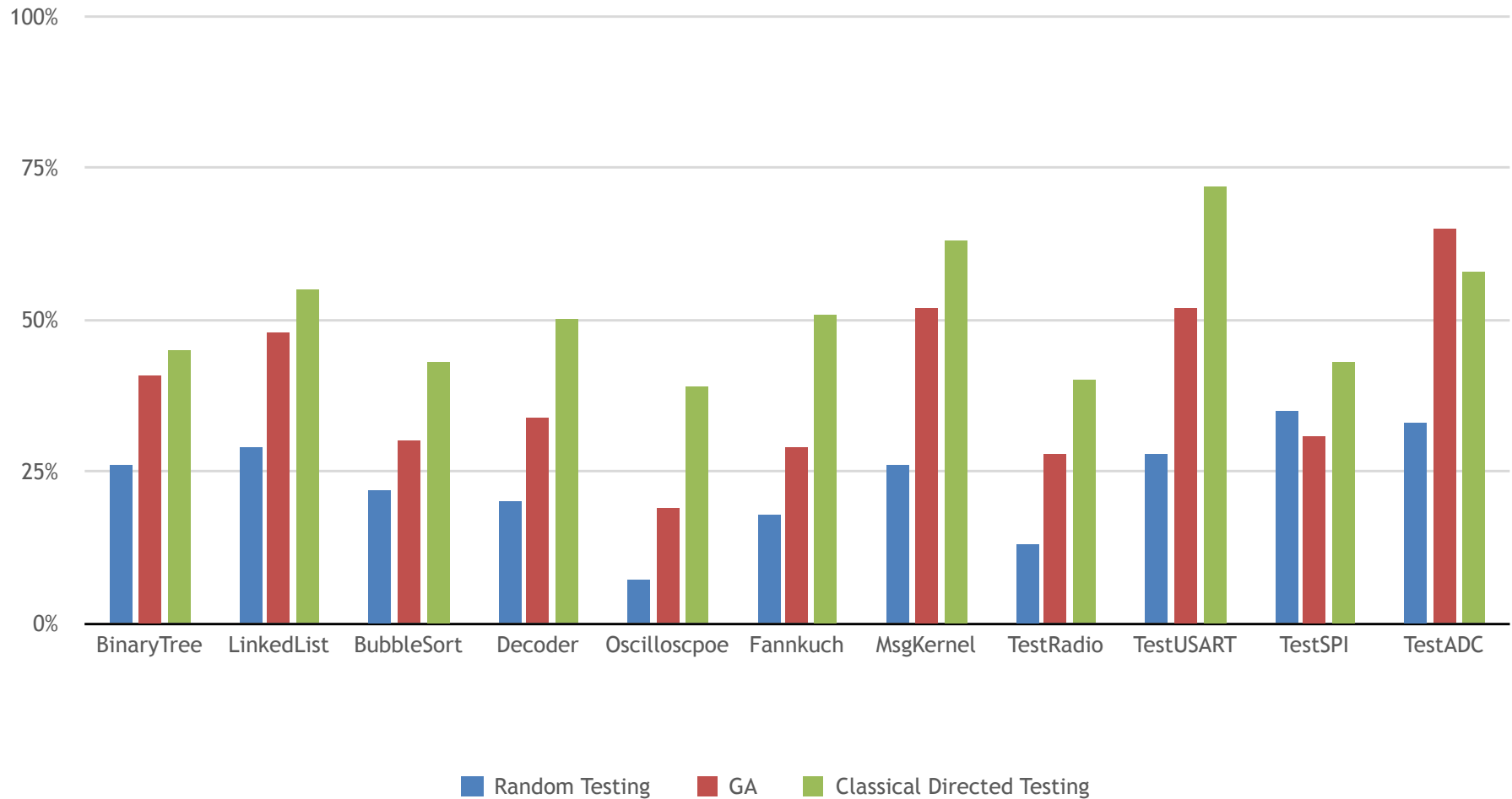
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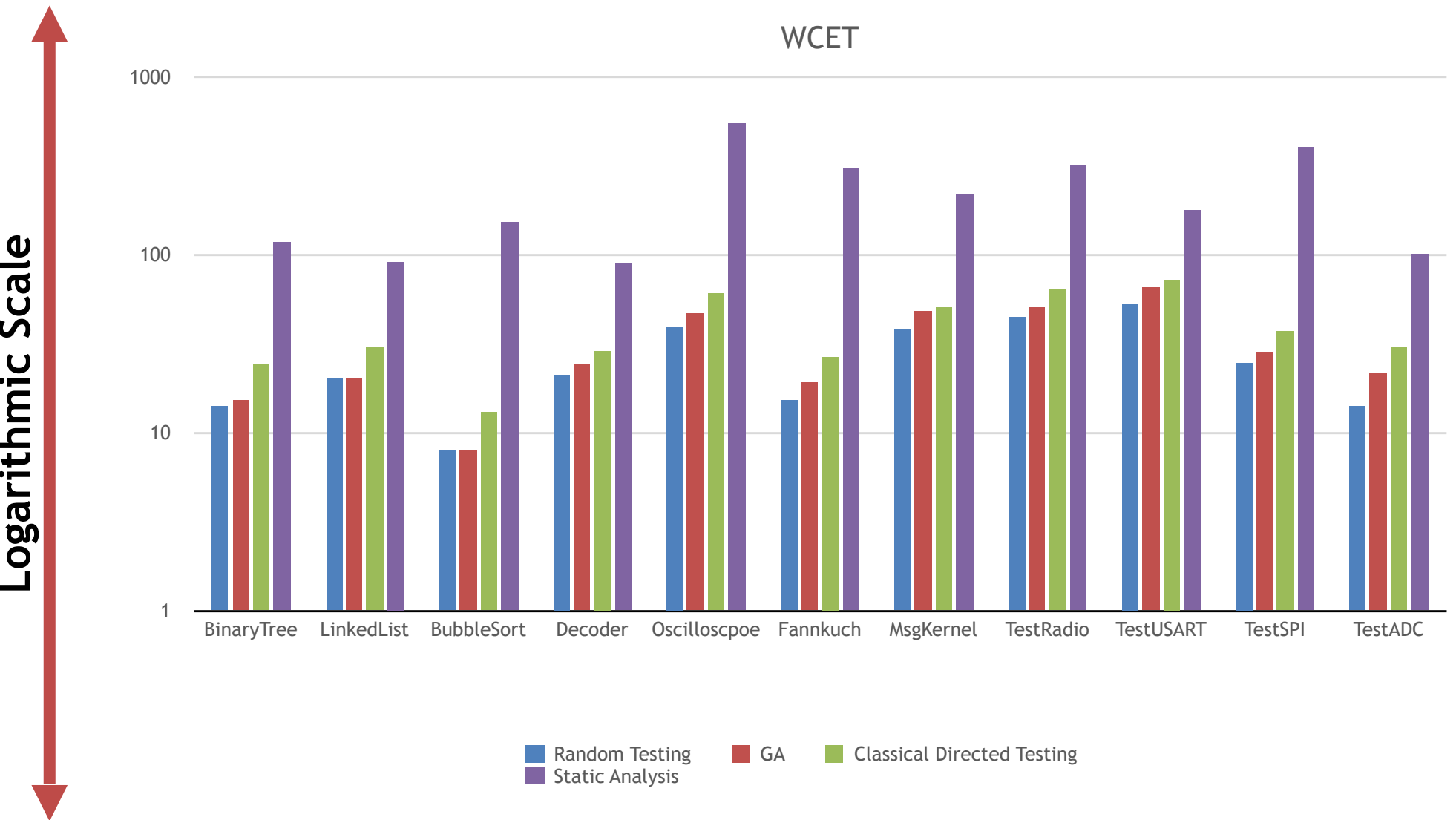
Motivating Experiments

branch coverage across testing techniques



Motivating Experiments

Testing VS Static Analysis of WCET



Testing Event Driven Software

- Classical software:
 - tester only devices a suite of single inputs.
- Event-Driven software (with real-time behavior):
 - tester must device a suite of *event sequences*.
 - In each sequence: # of events, types of events, values associated with the events e.g. registers' value, and timing of events.
- **Challenge:** Quickly generate a small number of challenging event sequences to improve branch coverage.

VICE Example

```
1 program Sample { entrypoint main = antenna.main, alt = antenna.alt; }
2 component test_antenna {
3   field sending:bool = false;
4   method main(data_1:int):void { dispatch_data(data_1); // code... }
5   method alt(data_2:int):void { dispatch_data(data_2); }
6   method dispatch_data(msg:int):void {
7     local res:int, tmp:int = random(100)
8     if (sending) return; else sending = true;
9     if (-2048 < msg && msg < 1024) res = check_and_send(msg,tmp);
10    sending = false;
11    return;
12  }
13  method check_and_send(s:int, t:int):int {
14    if (s==512)
15      return 1;
16    // send...
17    return 0;
18  }
19 }
```

Round 1

[<main,(723452)>,<alt1,(-10038)>,<main,_>,<alt1,_>]

Constraints: $data_1 = msg \wedge data2 = msg \wedge -2048 < msg \wedge msg < 1024$

Branch Coverage: 50% (3/6)

VICE Example

```
1 program Sample { entrypoint main = antenna.main, alt = antenna.alt; }
2 component test_antenna {
3   field sending:bool = false;
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11    return;
12  }
13  method check_and_send(s:int, t:int):int {
14    if (s==512)
15      return 1;
16    // send...
17    return 0;
18  }
19 }
```

Round 2

[<main,(-338)>, <alt1,(1001)>, <alt2,(6)>, <main, _>]

Constraints: $msg = s \wedge tmp = t \wedge s = 512$

Branch Coverage: 83% (5/6)

VICE Example

```
1 program Sample { entrypoint main = antenna.main, alt = antenna.alt; }
2 component test_antenna {
3   field sending:bool = false;
4   method main(data_1:int):void { dispatch_data(data_1); // code... }
5   method alt(data_2:int):void { dispatch_data(data_2); }
6   method dispatch_data(msg:int):void {
7     local res:int, tmp:int = random(100)
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11    return;
12  }
13  method check_and_send(s:int, t:int):int {
14    if (s==512)
15      return 1;
16    // send...
17    return 0;
18  }
19 }
```

Round 3

[<main,(-338)>,<alt1,(1001)>,<alt2,(6)>,<main, _>]

Constraints: $data1 = data2 = msg = s = 512$

Branch Coverage: 83% (5/6)

VICE Example

```
1 program Sample { entrypoint main = antenna.main, alt = antenna.alt; }
2 component test_antenna {
3   field sending:bool = false;
4   method main(data_1:int):void { dispatch_data(data_1); // code... }
5   method alt(data_2:int):void { dispatch_data(data_2); }
6   method dispatch_data(msg:int):void {
7     local res:int, tmp:int = random(100)
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10    sending = false;
11    return;
12  }
13  method check_and_send(s:int, t:int):int {
14    if (s==512)
15      return 1;
16    // send...
17    return 0;
18  }
19 }
```

Round 4

[<main,(512)>,<alt1,(512)>,<main,_>,<alt1, _>]

Constraints: -

Branch Coverage: 100% (6/6)

Event Based Directed Testing (EBDT)

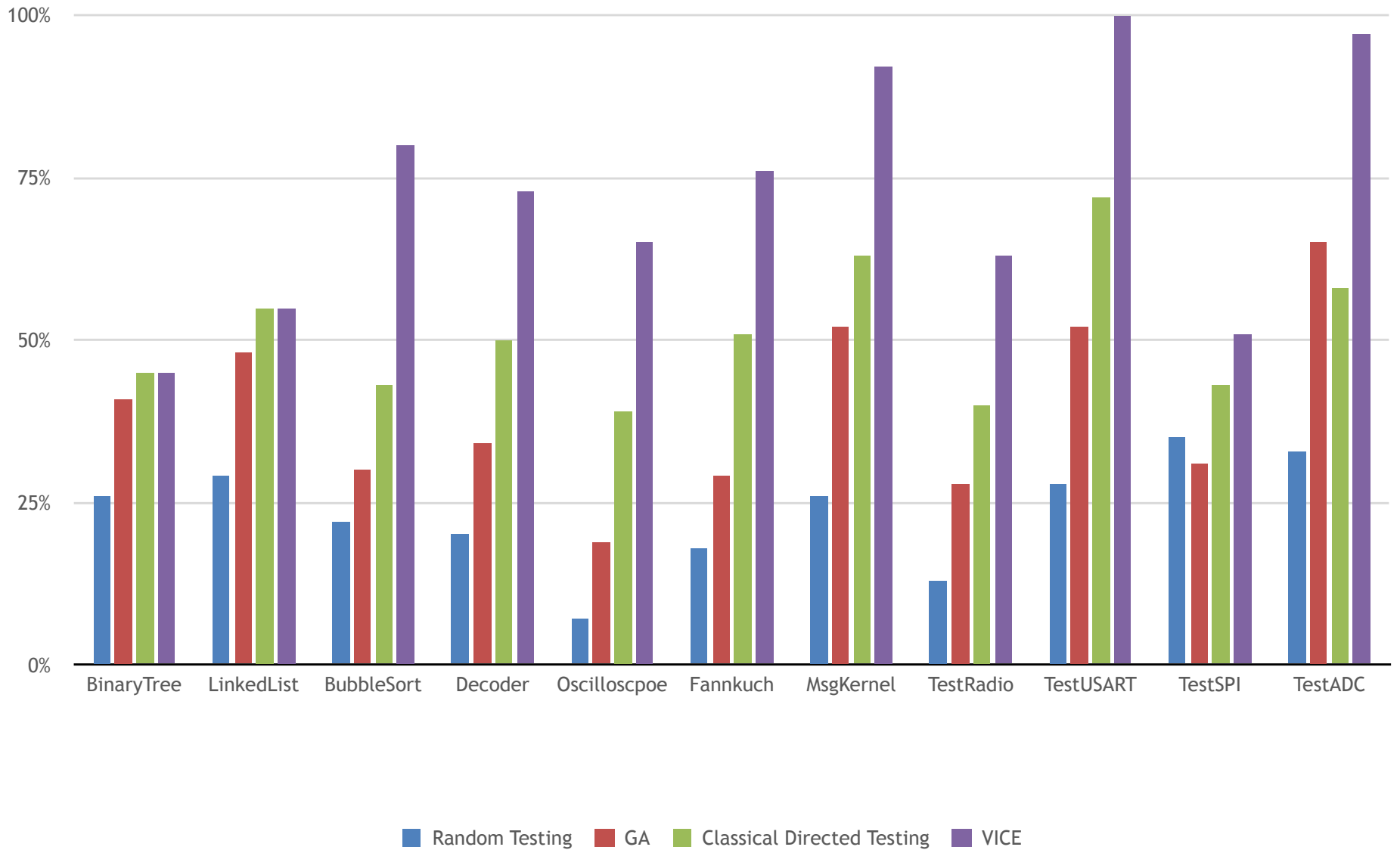
- **compiler:**
VirgilProgram → *machineCode*
- **avrora :**
machineCode × *eventSequence* → *wcet*
- **random:**
() → *eventSequence*
- **timeoutCombos:**
eventSequence → (*eventSequence list*)
- **concolic:**
(*Virgil program* × *eventSequence* →
wcet × *branchCoverage* × *constraints*)
- **solver:**
constraints → *solution*
- **generator:**
solution → *eventSequence*

Algorithm

Input: p : VirgilProgram
Output: $wcet \times \text{branchCoverage} \times \text{eventSequence}$
Local: a : machineCode = compiler(p), s : state = (0, 0.0, (), 0) , $roundId$: int = 0
 $seqs$: eventSequence = timeoutCombos(random())
Method: while ($s.noChange < 2$) {
 foreach (seq in $seqs$) {
 let ($wcet, bc, c$) = concolic(p, seq) in
 $s = \text{update}(s, wcet, bc, c, roundId)$
 }
 $roundId++$
 $seqs = \text{timeoutCombos}(\text{generator}(\text{solver}(s.constraints)))$
}
return ($s.wcet, s.coverage, s.eventSeq$)

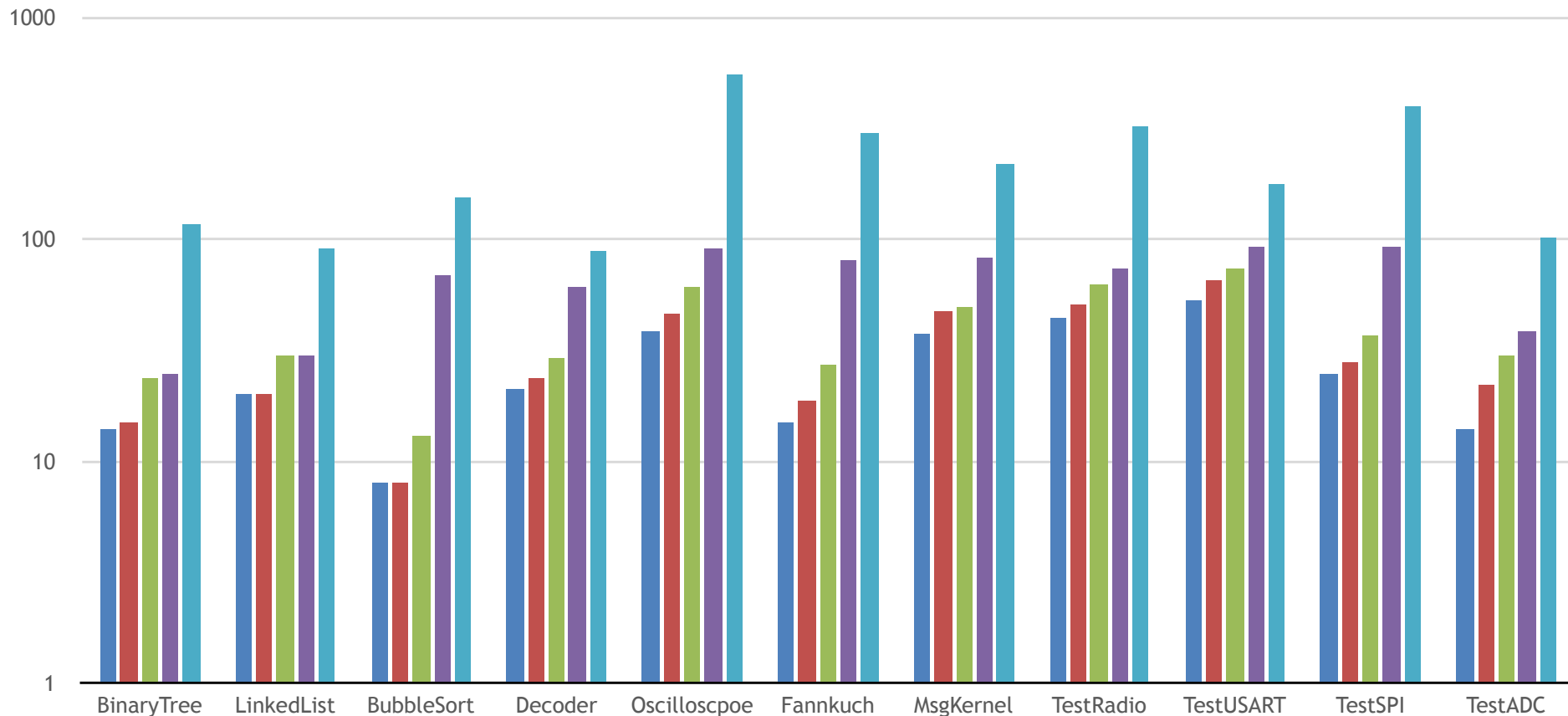
Experiment Results

Branch Coverage



Experiment Results

WCET



Random Testing GA Classical Directed Testing VICE
Static Analysis

Logarithmic Scale

Future Works

- Formulating timeouts symbolically
- Using some static information
 - Locate places where wcet happens, and direct execution towards candidates
 - Replace random event generation with a IMR-certified model checker.