Inicio > SORS: "Industrial Grade Open Source RISC-V"

SORS: "Industrial Grade Open Source RISC-V"

Abstract

Can you take open Source RISC-V and put it in a commercial application? Can you create Millions of chips based on Open source, can industrial and academia work together in open source? WHat are the benefits and how to make this collaboration happen? In this talk Flo, CEO of the non-profit OpenHW Group, will answer most of this question and introduce how the OpenHW Group enables a collaboration on RISC-V for industrial usage while keeping it open source and making it ready for commercial deployments (and he might tell us where they used already..). Flo also will introduce more of the OpenHW.

Short Bio

Flo is the CEO of the OpenHW Group and international not-for-Profit organization dedicated to curate and maintain high quality, industrial grade, open source RISC-V Cores. Flo is fascinated by RISC-V and help to smoothen accessibility to high quality RISC-V and help promote the RISC-V ISA worldwide. He is also one of the first RISC-V International Ambassadors. Before leading the OpenHW Group Flo was Head of Sales for a commercial RISC-V provider where he took care of the markets in Japan, Europe and Middle East.

Speakers

Speaker: Flo Wohlrab. CEO of OpenHW Group.
Host: Miquel Moretó. High Performance Domain-Specific Architectures, Computer Sciences, BSC. Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---