

Trace Cache Redundancy: Red & Blue Traces

Authors: [Ramirez, Alex](#) / [Larriba-Pey, Josep](#) / [Valero, Mateo](#)

Publication: Sixth International Symposium on High-Performance Computer Architecture (HPCA'2000)

Pagination: 325-333

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 15 Ago 2024 - 04:17): <https://www.bsc.es/es/research-and-development/publications/trace-cache-redundancy-red-blue-traces>