

[TMbox: A Flexible and Reconfigurable 16-core Hybrid Transactional Memory System](#)

Authors: [Sönmez, Nehir](#) / [Arcas, Oriol](#) / [Pflucker, Otto](#) / [Cristal, Adrián](#) / [Unsal, Osman](#) / [Hur, Ibrahim](#) / [Singh, Satnam](#) / [Valero, Mateo](#)

Publication: The 19th Annual IEEE International Symposium on Field-Programmable Custom Computing Machines (FCCM 2011)

Place Published: Salt Lake City, United States

Pagination: 1?8

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 18 Jul 2024 - 23:15): <https://www.bsc.es/es/research-and-development/publications/tmbox-flexible-and-reconfigurable-16-core-hybrid-transactional>