

[Inicio](#) > The limits of software transactional memory (STM): dissecting Haskell STM applications on a many-core environment

[The limits of software transactional memory \(STM\): dissecting Haskell STM applications on a many-core environment](#)

Authors: [Perfumo, Cristian](#) / [Sönmez, Nehir](#) / [Stipic, Srdjan](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Harris, Tim](#) / [Valero, Mateo](#)

Publication: Computing Frontiers '08

Pagination: 67?78

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 22 Dic 2024 - 09:45): <https://www.bsc.es/es/research-and-development/publications/the-limits-software-transactional-memory-stm-dissecting-0>