

[A Symbolic Emulator for Shuffle Synthesis on the NVIDIA PTX Code](#)

URL: <https://dl.acm.org/doi/10.1145/3578360.3580253>

Authors: [Matsumura, Kazuaki](#) / [de Gonzalo, Simon](#) / [Pena, Antonio](#)

Research Lines: [High Programming Productivity for Accelerators](#)

Publication: CC 2023: Proceedings of the 32nd ACM SIGPLAN International Conference on Compiler Construction

Pagination: 110-121

Palabras clave: [compiler](#), [Symbolic Analysis](#), [Code Generation](#), [GPUs](#), [NVIDIA PTX](#), [Program Optimization](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 13 Dic 2024 - 09:42): <https://www.bsc.es/es/research-and-development/publications/symbolic-emulator-shuffle-synthesis-the-nvidia-ptx-code>