

## [Software Time Reliability in the Presence of Cache Memories](#)

**URL:** [https://link.springer.com/chapter/10.1007/978-3-319-60588-3\\_15](https://link.springer.com/chapter/10.1007/978-3-319-60588-3_15)

**Authors:** [Milutinovic, Suzana](#) / [Abella, Jaume](#) / [Agirre, Irune](#) / [Azkarate-askasua, Mikel](#) / [Mezzetti, Enrico](#) / [Vardanega, Tullio](#) / [Cazorla, Francisco](#)

**Publication:** Ada Europe 2017 22nd International Conference on Reliable Software Technologies

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 15 Mar 2025 - 03:39):** <https://www.bsc.es/es/research-and-development/publications/software-time-reliability-the-presence-cache-memories>