

## **SMT Malleability in IBM POWER5 and POWER6 Processors**

**URL:** <http://capinfo.e.ac.upc.edu/PDFs/dir04/file004122.pdf>

**Authors:** [Morari, Alessandro](#) / [Boneti, Carlos](#) / [Cazorla, Francisco](#) / [Gioiosa, Roberto](#) / [Cher, Chen-Yong](#) / [Buyuktosunoglu, Alper](#) / [Bose, Pradip](#) / [Valero, Mateo](#)

**Publication:** IEEE Transactions on Computers

**Volume / Number:** 00 / 99

**Palabras clave:** [IBMBSC](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 14 Dic 2024 - 16:31):** <https://www.bsc.es/es/research-and-development/publications/smt-malleability-ibm-power5-and-power6-processors>