

[Simulating Whole Supercomputer Applications](#)

Authors: [González, Juan](#) / [Giménez, Judit](#) / [Casas, Marc](#) / [Moreto, Miquel](#) / [Ramirez, Alex](#) / [Labarta, Jesús](#) / [Valero, Mateo](#)

Publication: IEEE Micro

Volume / Pagination: 31 / 32-45

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 17 Sep 2024 - 06:46): <https://www.bsc.es/es/research-and-development/publications/simulating-whole-supercomputer-applications>