

## [SEDEA: A Sensible Approach to Account DRAM Energy in Multicore Systems](#)

**URL:** <http://ieeexplore.ieee.org/document/8102180/>

**Authors:** [Liu, Qixiao](#) / [Moreto, Miquel](#) / [Abella, Jaume](#) / [Cazorla, Francisco](#) / [Valero, Mateo](#)

**Publication:** 2017 29th International Symposium on Computer Architecture and High Performance Computing (SBAC-PAD)2017 29th International Symposium on Computer Architecture and High Performance Computing (SBAC-PAD)

**Place Published:** Campinas, Brazil

**Pagination:** 73 - 80

**Palabras clave:** [IBMBSC](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 16 Sep 2024 - 07:54):** <https://www.bsc.es/es/research-and-development/publications/sedeasensibleapproachaccountdramenergymulticore-systems>