

Reducing Simulation Time

Authors: [Moreto, Miquel](#) / [Ramirez, Alex](#) / [Valero, Mateo](#)

Publication: 2006 Advanced Computer Architecture and Compilation for Embedded Systems (ACACES-06)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 14 Mar 2025 - 23:29): <https://www.bsc.es/es/research-and-development/publications/reducing-simulation-time>