

[Reducing Cache Coherence Traffic with Hierarchical Directory Cache and NUMA-Aware Runtime Scheduling](#)

URL: <http://dl.acm.org/citation.cfm?doid=2967938.2967962>

UPCommons Handle URL <http://upcommons.upc.edu/handle/2117/96470>

Authors: [Caheny, Paul](#) / [Casas, Marc](#) / [Moreto, Miquel](#) / [Gloaguen, Hervé](#) / [Saintes, Maxime](#) / [Aiguade, Eduard](#) / [Labarta, Jesús](#) / [Valero, Mateo](#)

Publication: Proceedings of the 2016 International Conference on Parallel Architectures and Compilation - PACT '16

Place Published: Haifa, Israel

Pagination: 275 - 286

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 27 Sep 2024 - 12:18): <https://www.bsc.es/es/research-and-development/publications/reducing-cache-coherence-traffic-hierarchical-directory-cache>