

## [Optimal Use of Registers in Aggressive Superscalar Processors](#)

**Authors:** [Cristal, Adrián](#) / [Martínez, José](#) / [Llosa, Josep](#) / [Valero, Mateo](#)

**Publication:** XIV Jornadas de Paralelismo

**Place Published:** Leganés, Spain

**Pagination:** 553?558

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 28 Sep 2024 - 10:35):** <https://www.bsc.es/es/research-and-development/publications/optimal-use-registers-aggressive-superscalar-processors>