

## [Modelling Probabilistic Cache Representativeness in the Presence of Arbitrary Access Patterns](#)

**Authors:** [Milutinovic, Suzana](#) / [Abella, Jaume](#) / [Cazorla, Francisco](#)

**Publication:** ISORC 2016 19th IEEE Symposium On Real-Time Computing

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 15 Mar 2025 - 07:12):** <https://www.bsc.es/es/research-and-development/publications/modelling-probabilistic-cache-representativeness-the-presence>