

[Modeling Stencil Computations on Modern HPC Architectures](#)

Authors: [de la Cruz, Raúl](#) / [Araya-Polo, Mauricio](#)

Publication: 5th International Workshop on Performance Modeling, Benchmarking and Simulation of High Performance Computer Systems (PMBS14) held as part of SC14

Palabras clave: [FD](#), [HPC](#), [Intel Xeon Phi](#), [Modeling](#), [multi-core](#), [prefetching](#), [Semi-stencil](#), [spatial blocking](#), [Stencil computation](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 9 Nov 2024 - 06:52): <https://www.bsc.es/es/research-and-development/publications/modeling-stencil-computations-modern-hpc-architectures>