Low-Precision Floating-Point Schemes for Neural Network Training

URL: https://arxiv.org/abs/1804.05267

Authors: Ortiz, Marc / Cristal, Adrián / Ayguadé, Eduard / Casas, Marc

Research Lines: Runtime aware architectures

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on *10 Mar 2025 - 18:04*): https://www.bsc.es/es/research-and-development/publications/low-precision-floating-point-schemes-neural-network-training