

A Low-Complexity Fetch Architecture for High-Performance Superscalar Processors

Authors: [Santana, Oliverio](#) / [Ramirez, Alex](#) / [Larriba-Pey, Josep](#) / [Valero, Mateo](#)

Publication: ACM Transactions on Architecture and Code Optimization

Volume / Pagination: 1 / 220-245

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 1 Ago 2024 - 20:29): <https://www.bsc.es/es/research-and-development/publications/low-complexity-fetch-architecture-high-performance-superscal-0>