

[LAEC: Look-Ahead Error Correction Codes in Embedded Processors L1 Data Cache](#)

URL: <https://ieeexplore.ieee.org/document/8714877>

Authors: [Benedicte, Pedro](#) / [Hernandez, Carles](#) / [Abella, Jaume](#) / [Cazorla, Francisco](#)

Publication: 2019 Design, Automation & Test in Europe Conference & Exhibition (DATE)

Place Published: IEEE

Pagination: 818-823

Palabras clave: [Delays](#), [Error correction](#), [error correction codes](#), [Hardware](#), [Multicore processing](#), [Program processors](#), [Registers](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 13 Dic 2024 - 13:34): <https://www.bsc.es/es/research-and-development/publications/laec-look-ahead-error-correction-codes-embedded-processors-l1>