

[Increasing the Reliability of Software Timing Analysis for Cache-Based Processors](#)

URL: <https://ieeexplore.ieee.org/document/8603793/>

Authors: [Milutinovic, Suzana](#) / [Mezzetti, Enrico](#) / [Abella, Jaume](#) / [Cazorla, Francisco](#)

Publication: IEEE Transactions on Computers

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 15 Jul 2024 - 01:22): <https://www.bsc.es/es/research-and-development/publications/increasing-the-reliability-software-timing-analysis-cache>