

## **HPCG on long-vector architectures: Evaluation and optimization on NEC SX-Aurora and RISC-V**

**URL:** <https://linkinghub.elsevier.com/retrieve/pii/S0167739X23000225>

**Authors:** [Gomez, Constantino](#) / [Mantovani, Filippo](#) / [Focht, Erich](#) / [Casas, Marc](#)

**Research Lines:** [Mobile and embedded-based HPC](#)

**Publication:** Future Generation Computer Systems

**Volume / Pagination:** 14301830303037 / 152 - 162

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 8 Jul 2024 - 20:32):** <https://www.bsc.es/es/research-and-development/publications/hpcg-long-vector-architectures-evaluation-and-optimization-n-0>