

[Hardware Transactional Memory with Operating System Support, HTMOS](#)

Authors: [Tomi?, Sa?a](#) / [Cristal, Adrián](#) / [Unsal, Osman](#) / [Valero, Mateo](#)

Publication: Workshop on Highly Parallel Processing on a Chip in conjunction with Euro-Par

Place Published: IRISA, Rennes, France

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 13 Dic 2024 - 23:57): <https://www.bsc.es/es/research-and-development/publications/hardware-transactional-memory-operating-system-support-htmos>