

[Hard Real-Time Capable Multicore Processors for Space Applications](#)

Authors: [Moreto, Miquel](#) / [Paolieri, Marco](#) / [Abella, Jaume](#) / [Quinones, Eduardo](#) / [Cazorla, Francisco](#) / [Valero, Mateo](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 15 Mar 2025 - 00:02): <https://www.bsc.es/es/research-and-development/publications/hard-real-time-capable-multicore-processors-space-applications>