

[FaultTM: Fault-Tolerance Using Hardware Transactional Memory](#)

Authors: [Yalcin, Gulay](#) / [Unsal, Osman](#) / [Hur, Ibrahim](#) / [Cristal, Adrián](#)

Publication: Workshop on Parallel Execution of Sequential Programs on Multi-core Architecture (PESPMA)

Place Published: Saint Malo, French Guiana

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 18 Jul 2024 - 23:34): <https://www.bsc.es/es/research-and-development/publications/faultm-fault-tolerance-using-hardware-transactional-memory>