

[Inicio](#) > Exploring Energy Reduction in Future Technology Nodes via Voltage Scaling with Application to 10nm

---

## [Exploring Energy Reduction in Future Technology Nodes via Voltage Scaling with Application to 10nm](#)

**URL:** <http://ieeexplore.ieee.org/document/7445329/>

**Authors:** [Yalcin, Gulay](#) / [Rethinagiri, Santhosh](#) / [Palomar, Oscar](#) / [Unsal, Osman](#) / [Cristal, Adrian](#) / [Milojevic, Dragomir](#)

**Publication:** 24th Euromicro International Conference on Parallel, Distributed, and Network-Based Processing (PDP)

**Place Published:** Heraklion, Crete, Greece

**Pagination:** 184 - 191

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 14 Mar 2025 - 12:35):** <https://www.bsc.es/es/research-and-development/publications/exploring-energy-reduction-future-technology-nodes-voltage>