

## [Evolutionary system for prediction and optimization of hardware architecture performance](#)

**Authors:** [Castillo, Pedro](#) / [Merelo, Juan Julian](#) / [Moreto, Miquel](#) / [Cazorla, Francisco](#) / [Valero, Mateo](#) / [Mora, Antonio](#) / [Laredo, Juan Luís](#) / [McKee, Sally](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 26 Sep 2024 - 23:43):** <https://www.bsc.es/es/research-and-development/publications/evolutionary-system-prediction-and-optimization-hardware>