

[Inicio](#) > Evaluating the effect of last-level cache sharing on integrated GPU-CPU systems with heterogeneous applications

---

## [Evaluating the effect of last-level cache sharing on integrated GPU-CPU systems with heterogeneous applications](#)

**URL:** <http://ieeexplore.ieee.org/document/7581277/>

**UPCommons Handle URL** <http://upcommons.upc.edu/handle/2117/96866>

**Authors:** [Garcia, Victor](#) / [Gomez-Luna, Juan](#) / [Grass, Thomas](#) / [Rico, Alejandro](#) / [Ayguade, Eduard](#) / [Pena, Antonio](#)

**Research Lines:** [Memory hierarchy for GPU acceleration](#)

**Publication:** Workload Characterization (IISWC), 2016 IEEE International Symposium on

**Place Published:** Providence, RI, USA

**Pagination:** 168-177

**Palabras clave:** [GPUs](#), [heterogeneous architectures](#), [memory hierarchy](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 24 Dic 2024 - 21:17):** <https://www.bsc.es/es/research-and-development/publications/evaluating-the-effect-last-level-cache-sharing-integrated-gpu>