

Efficient Execution of SpGEMM on Long Vector Architectures

Authors: [Le Fèvre, Valentin](#) / [Casas, Marc](#)

Research Lines: [Software research and development vehicles for New ARchitectures \(SONAR\)](#)

Publication: 32nd International Symposium on High-Performance Parallel and Distributed Computing (HPDC)

Pagination: 101-113

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 16 Jul 2024 - 19:37): <https://www.bsc.es/es/research-and-development/publications/efficient-execution-spgemm-long-vector-architectures>