

[EazyHTM, Eager-Lazy Hardware Transactional Memory](#)

URL: <http://capinfo.e.ac.upc.edu/PDFs/dir07/file003458.pdf>

Authors: [Tomi?, Sa?a](#) / [Perfumo, Cristian](#) / [Kulkarni, Chinmay](#) / [Armejach, Adrià](#) / [Cristal, Adrián](#) / [Unsal, Osman](#) / [Harris, Tim](#) / [Valero, Mateo](#)

Publication: 42nd International Symposium on Microarchitecture (MICRO)

Place Published: New York, United States

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 15 Dic 2024 - 07:37): <https://www.bsc.es/es/research-and-development/publications/eazyhtm-eager-lazy-hardware-transactional-memory>