

## [A Directive-Based Approach to Perform Persistent Checkpoint/Restart](#)

**URL:** <http://ieeexplore.ieee.org/document/8035111/>

**Authors:** [Maroñas, Marcos](#) / [Mateo, Sergi](#) / [Beltran, Vicenç](#) / [Ayguade, Eduard](#)

**Research Lines:** [HPC Software Optimization](#) / [Resilient architecture and runtimes](#)

**Publication:** The 2017 International Conference on High Performance Computing & Simulation (HPCS2017)

**Palabras clave:** [checkpoint/restart](#); [resiliency](#); [fault tolerance](#); [exascale](#); [programmability](#); [programming models](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 14 Dic 2024 - 11:15):** <https://www.bsc.es/es/research-and-development/publications/directive-based-approach-perform-persistent-checkpointrestart>