

[Design and Implementation of a Time Predictable Processor: Evaluation With a Space Case Study](#)

Authors: [Hernández, Carles](#) / [Abella, Jaume](#) / [Cazorla, Francisco](#) / [Bardizbanyan, Alen](#) / [Andersson, Jan](#) / [Cros, Fabrice](#) / [Wartel, Franck](#)

Research Lines: [Dependable and power-efficient real-time systems](#) / [Probabilistic Real Time Systems](#)

Publication: Euromicro Conference on Real-Time Systems

Pagination: 1-16

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 Nov 2024 - 08:10): <https://www.bsc.es/es/research-and-development/publications/design-and-implementation-time-predictable-processor>