

[Inicio](#) > cuThomasBatch and cuThomasVBatch, CUDA Routines to compute batch of tridiagonal systems on NVIDIA GPUs

[cuThomasBatch and cuThomasVBatch, CUDA Routines to compute batch of tridiagonal systems on NVIDIA GPUs](#)

URL: <http://dx.doi.org/10.1002/cpe.4909>

Authors: [Valero-Lara, Pedro](#) / [Martinez-Perez, Ivan](#) / [Sirvent, Raul](#) / [Martorell, Xavier](#) / [Pena, Antonio](#)

Research Lines: [Application optimization for GPU acceleration](#)

Publication: Concurrency and Computation Practice and Experience

Place Published: Wiley

Volume / Number / Pagination: 30 / 24 / 1-10

Palabras clave: [CR](#), [CUDA](#), [cuSPARSE](#), [parallelprocessing](#), [PCR](#), [Scalability](#), [Thomasalgorithm](#), [tridiagonallinear systems](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 Nov 2024 - 12:42): <https://www.bsc.es/es/research-and-development/publications/cuthomasbatch-and-cuthomasvbatch-cuda-routines-compute-batch-0>