

[Combining Dynamic Concurrency Throttling with Voltage and Frequency Scaling on Task-based Programming Models](#)

Authors: [Navarro, Antoni](#) / [Lorenzo, Arthur](#) / [Parra, Eduard](#) / [Beltran, Vicenç](#)

Publication: ICPP 2021: 50th International Conference on Parallel Processing

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 5 Nov 2024 - 20:26): <https://www.bsc.es/es/research-and-development/publications/combining-dynamic-concurrency-throttling-voltage-and-frequency>