

[Coherence Protocol for Transparent Management of Scratchpad Memories in Shared Memory Manycore Architectures](#)

URL: <http://dl.acm.org/citation.cfm?doid=2749469.2750411>

Authors: [Alvarez, Lluc](#) / [Vilanova, Lluís](#) / [Moreto, Miquel](#) / [Casas, Marc](#) / [González, Marc](#) / [Martorell, Xavier](#) / [Navarro, Nacho](#) / [Aguade, Eduard](#) / [Valero, Mateo](#)

Publication: Proceedings of the 42nd International Symposium on Computer Architecture (ISCA)

Place Published: Portland, Oregon

Pagination: 720-732

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 18 Jul 2024 - 00:36): <https://www.bsc.es/es/research-and-development/publications/coherence-protocol-transparent-management-scratchpad-memories>