

## CleanET: enabling timing validation for complex automotive systems

**URL:** <https://dl.acm.org/doi/10.1145/3341105.3373871>

**Authors:** [Vilardell, Sergi](#) / [Serra, Isabel](#) / [Tabani, Hamid](#) / [Abella, Jaume](#) / [Del Castillo, Joan](#) / [Cazorla, Francisco](#)

**Research Lines:** [COTS multicore real-time systems](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 26 Abr 2025 - 22:55):** <https://www.bsc.es/es/research-and-development/publications/cleanet-enabling-timing-validation-complex-automotive-systems>