

[Branch Classification to Control Instruction Fetch in Simultaneous Multithreaded Architectures](#)

Authors: [Knijnenburg, Peter](#) / [Ramirez, Alex](#) / [Latorre, Fernando](#) / [Larriba-Pey, Josep](#) / [Valero, Mateo](#)

Publication: International Workshop on Innovative Architecture (IWIA 2002)

Place Published: Kohala Coast, Hawaii (United States)

Pagination: 67-76

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 18 Oct 2024 - 15:15): <https://www.bsc.es/es/research-and-development/publications/branch-classification-control-instruction-fetch-simultaneous>