

[Inicio](#) > Analyzing the effect of last level cache sharing on integrated platforms with fine-grain CPU-GPU collaboration

Analyzing the effect of last level cache sharing on integrated platforms with fine-grain CPU-GPU collaboration

Authors: [Garcia, Victor](#) / [Gomez-Luna, Juan](#) / [Grass, Thomas](#) / [Rico, Alejandro](#) / [Pena, Antonio](#) / [Ayuade, Eduard](#)

Research Lines: [Memory hierarchy for GPU acceleration](#)

Publication: GPU Technology Conference Europe (GTC Europe)

Place Published: Amsterdam, The Netherlands

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 27 Sep 2024 - 06:58): <https://www.bsc.es/es/research-and-development/publications/analyzing-the-effect-last-level-cache-sharing-integrated>