

Bus Designs for Time-Probabilistic Multicore Processors

Authors: [Jalle, Javier](#) / [Kosmidis, Leonidas](#) / [Abella, Jaume](#) / [Quinones, Eduardo](#) / [Cazorla, Francisco](#)

Research Lines: [Probabilistic Real Time Systems](#)

Teams: [Computer Architecture - Operating Systems \(CAOS\)](#)

Publication: Design, Automation and Test in Europe (DATE) 2014

Place Published: Dresden, Germany

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 Nov 2024 - 06:58): <https://www.bsc.es/es/node/40841>