

## [Analyzing the Efficiency of L1 Caches for Reliable Hybrid-Voltage Operation Using EDC Codes](#)

**Authors:** [Maric, Bojan](#) / [Abella, Jaume](#) / [Valero, Mateo](#)

**Publication:** IEEE Transactions on Very Large Scale Integration (VLSI) Systems

**Volume / Pagination:** 22 / 266-275

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 16 Mar 2025 - 00:04):** <https://www.bsc.es/es/node/40815>