

[Implicit-Storing and Redundant-Encoding-of-Attribute Information in Error-Correction-Codes](#)

Authors: [Sazeides, Yiannakis](#) / [Ozer, Emre](#) / [Kershaw, Danny](#) / [Nikolaou, Panagiota](#) / [Kleanthous, Marios](#) / [Abella, Jaume](#)

Teams: [Computer Architecture - Operating Systems \(CAOS\)](#)

Publication: 46th IEEE/ACM International Symposium on Microarchitecture (MICRO)

Place Published: Davis (California), United States

Pagination: 160?171

Palabras clave: [error code tagging](#), [error correction codes](#), [implicit storing](#), [memory](#), [redundant encoding](#), [Reliability](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 16 Mar 2025 - 02:40): <https://www.bsc.es/es/node/40603>