

Supporting Industrial Use of Probabilistic Timing Analysis with Explicit Argumentation

Authors: [Stephenson, Zoe](#) / [Abella, Jaume](#) / [Vardanega, Tullio](#)

Teams: [Computer Architecture - Operating Systems \(CAOS\)](#)

Publication: 11th IEEE International Conference on Industrial Informatics (INDIN)

Place Published: Bochum, Germany

Pagination: 734?740

Palabras clave: [Computer architecture](#), [critical real-time embedded systems](#), [Embedded systems](#), [explicit argumentation](#), [Hardware](#), [MBPTA](#), [probabilistic exceedance functions](#), [Probabilistic logic](#), [probabilistic timing analysis](#), [Safety](#), [Software](#), [software programs](#), [synchronisation](#), [systems analysis](#), [Timing](#), [WCET analysis](#), [worst-case execution time analysis](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 13 Dic 2024 - 15:35): <https://www.bsc.es/es/node/40599>