

VSR sort: A novel vectorised sorting algorithm & architecture extensions for future microprocessors

URL: <http://ieeexplore.ieee.org/document/7056019/>

Authors: [Hayes, Timothy](#) / [Palomar, Oscar](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Valero, Mateo](#)

Publication: 2015 IEEE 21st International Symposium on High Performance Computer Architecture (HPCA)

Place Published: Burlingame, CA, USA

Pagination: 26-38

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 24 nov 2024 - 12:01): <https://www.bsc.es/ca/research-and-development/publications/vsr-sort-novel-vectorised-sorting-algorithm-architecture>