

Trace Cache Redundancy

Authors: [Ramirez, Alex](#) / [Larriba-Pey, Josep](#) / [Navarro, Carlos](#) / [Serrano, Xavier](#) / [Torrellas, Josep](#) / [Valero, Mateo](#)

Publication: X Jornadas de Paralelismo

Pagination: 39-44

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 27 set 2024 - 00:03): <https://www.bsc.es/ca/research-and-development/publications/trace-cache-redundancy>