

[TMbox: A Flexible and Reconfigurable 16-Core Hybrid Transactional Memory System](#)

Authors: [Sonmez, Nehir](#) / [Arcas, Oriol](#) / [Pflucker, Otto](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Hur, Ibrahim](#) / [Singh, Satnam](#) / [Valero, Mateo](#)

Publication: Proc. FCCM '11

Pagination: 146?153

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 5 febr 2025 - 05:48): <https://www.bsc.es/ca/research-and-development/publications/tmbox-flexible-and-reconfigurable-16-core-hybrid-transaction-1>