

[Inici](#) > Scalable multi-chip quantum architectures enabled by cryogenic hybrid wireless/quantum-coherent network-in-package

---

## [Scalable multi-chip quantum architectures enabled by cryogenic hybrid wireless/quantum-coherent network-in-package](#)

**Authors:** [Alarcón, Eduard](#) / [Abadal, Sergi](#) / [Sebastiano, Fabio](#) / [Babaie, Masoud](#) / [Charbon, Edoardo](#) / [Bolívar, Peter](#) / [Palesi, Maurizio](#) / [Blokhina, Elena](#) / [Leipold, Dirk](#) / [Staszewski, Bogdan](#) / [Garcia-Sáez, Artur](#) / [Almudever, Carmen](#)

**Research Lines:** [Quantum Algorithms](#)

**Publication:** 2023 IEEE International Symposium on Circuits and Systems (ISCAS)

**Pagination:** 1-5

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 15 Mar 2025 - 10:23):** <https://www.bsc.es/ca/research-and-development/publications/scalable-multi-chip-quantum-architectures-enabled-cryogenic>