

[SafeTI: a Hardware Traffic Injector for MPSoC Functional and Timing Validation](#)

URL: <https://ieeexplore.ieee.org/document/9486689/>

Authors: [Sala, Oriol](#) / [Alcaide, Sergi](#) / [Cabo, Guillem](#) / [Bas, Francisco](#) / [Lorenzo, Ruben](#) / [Benedicte, Pedro](#) / [Trilla, David](#) / [Gil, Guillermo](#) / [Mazzocchetti, Fabio](#) / [Abella, Jaume](#)

Publication: 2021 IEEE 27th International Symposium on On-Line Testing and Robust System Design (IOLTS)

Place Published: Torino, Italy

Volume / Pagination: 5339 / 1 - 7

Paraules clau: [SELENE](#), [MPSoC](#), [RISC-V](#), [Safety Critical Systems](#), [Testing](#), [timing validation](#), [verification and validation](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 15 set 2024 - 20:15): <https://www.bsc.es/ca/research-and-development/publications/safeti-hardware-traffic-injector-mpsoc-functional-and-timing>