

Inici > Performance Impact of the Interconnection Network on MareNostrum Applications

## Performance Impact of the Interconnection Network on MareNostrum Applications

Authors: Ramirez, Alex / Prat, / Labarta, Jesús / Valero, Mateo

**Publication:** 1st Workshop on Interconnection Network Architectures: On-Chip, Multi-Chip (INA-OCMC 2007)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

**Source URL** (**retrieved on** *14 ago 2024 - 19:26*): <a href="https://www.bsc.es/ca/research-and-development/publications/performance-impact-the-interconnection-network-marenostrum">https://www.bsc.es/ca/research-and-development/publications/performance-impact-the-interconnection-network-marenostrum</a>