

[Inici](#) > Paving the way towards a highly energy-efficient and highly integrated compute node for the Exascale revolution: the ExaNoDe approach

---

## [Paving the way towards a highly energy-efficient and highly integrated compute node for the Exascale revolution: the ExaNoDe approach](#)

**URL:** <http://exanode.eu/euromicro-dsdseaa-2017-30-august-1-september-2017-vienna-austria/>

**Authors:** [Rigo, Alvise](#) / [Pinto, Christian](#) / [Pouget, Kevin](#) / [Raho, Daniel](#) / [Dutoit, Denis](#) / [Martinez, Pierre-Yves](#) / [Doran, Chris](#) / [Benini, Luca](#) / [Mavroidis, Iakovos](#) / [Marazakis, Manolis](#) / [Bartsch, Valeria](#) / [Lonsdale, Guy](#) / [Pop, Antoniu](#) / [Goodacre, John](#) / [Colliot, Annaik](#) / [Carpenter, Paul](#) / [Radojkovic, Petar](#) / [Pleiter, Dirk](#) / [Drouin, Dominique](#) / [de Dinechin, Benoit](#)

**Research Lines:** [Memory systems for HPC and AI](#) / [Microserver architectures and system software](#)

**Publication:** Euromicro Symposium on Digital System Design, DSD 2017

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 21 oct 2024 - 02:32):** <https://www.bsc.es/ca/research-and-development/publications/paving-the-way-towards-highly-energy-efficient-and-highly>