MUCH: Exploiting Pairwise Hardware Event Monitor Correlations for Improved Timing Analysis of Complex MPSoCs

URL: https://dl.acm.org/doi/abs/10.1145/3412841.3441931

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Research Lines: COTS multicore real-time systems

Publication: MUCH: Exploiting Pairwise Hardware Event Monitor Correlations for Improved Timing Analysis of Complex MPSoCs

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Source URL (retrieved on *15 Mar 2025 - 04:57*): https://www.bsc.es/ca/research-and-development/publications/much-exploiting-pairwise-hardware-event-monitor-correlations