

Inici > Modeling High-Performance Wormhole NoCs for Critical Real-Time Embedded Systems

Modeling High-Performance Wormhole NoCs for Critical Real-Time Embedded Systems

URL: http://ieeexplore.ieee.org/document/7461342/

Authors: Panic, Milos / Hernández, Carles / Quiñones, Eduardo / Abella, Jaume / Cazorla, Francisco

Research Lines: COTS multicore real-time systems

Publication: 2016 IEEE Real-Time and Embedded Technology and Applications Symposium (RTAS)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on *10 Mar 2025 - 01:49*): https://www.bsc.es/ca/research-and-development/publications/modeling-high-performance-wormhole-nocs-critical-real-time